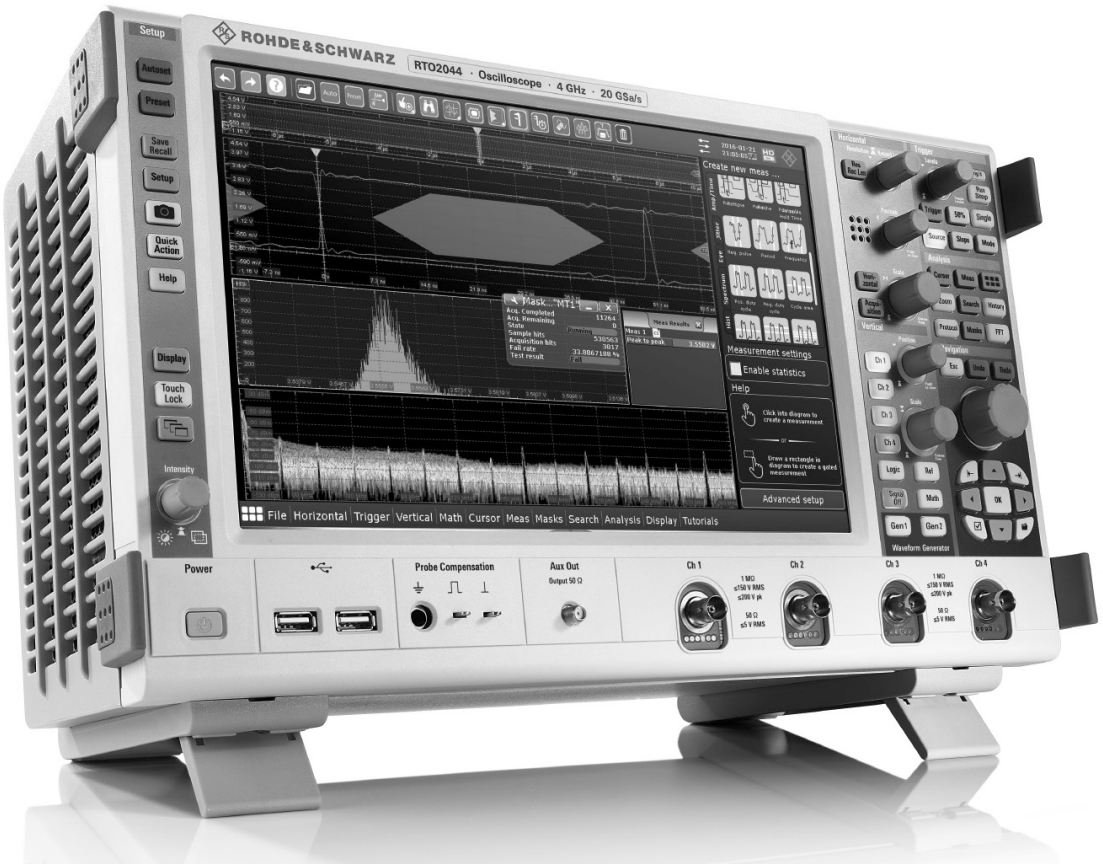
R&S®RTO
Digital Oscilloscope
Specifications



ROHDE&SCHWARZ

Test & Measurement

Data Sheet | 03.00

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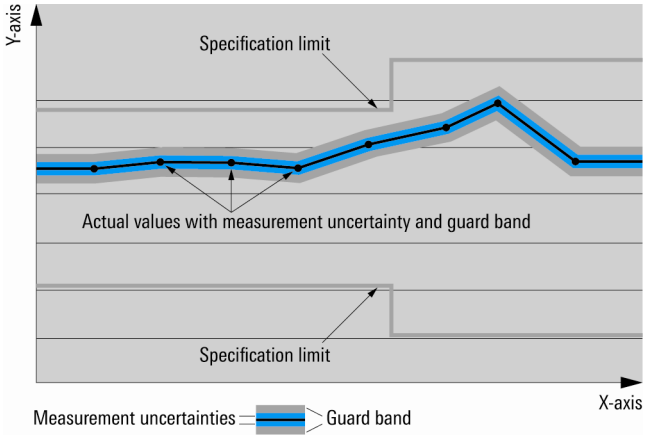
Definitions

General
Product data applies under the following conditions:

- Three hours storage at ambient temperature followed by 30 minutes warm-up operation
- Specified environmental conditions met
- Recommended calibration interval adhered to
- All internal automatic adjustments performed, if applicable

Specifications with limits
Represent warranted product performance by means of a range of values for the specified parameter. These specifications are marked with limiting symbols such as <, ≤, >, ≥, ±, or descriptions such as maximum, limit of, minimum. Compliance is ensured by testing or is derived from the design. Test limits are narrowed by guard bands to take into account measurement uncertainties, drift and aging, if applicable.



Specifications without limits
Represent warranted product performance for the specified parameter. These specifications are not specially marked and represent values with no or negligible deviations from the given value (e.g. dimensions or resolution of a setting parameter). Compliance is ensured by design.

Typical data (typ.)
Characterizes product performance by means of representative information for the given parameter. When marked with <, > or as a range, it represents the performance met by approximately 80 % of the instruments at production time. Otherwise, it represents the mean value.

Nominal values (nom.)
Characterize product performance by means of a representative value for the given parameter (e.g. nominal impedance). In contrast to typical data, a statistical evaluation does not take place and the parameter is not tested during production.

Measured values (meas.)
Characterize expected product performance by means of measurement results gained from individual samples.

Uncertainties
Represent limits of measurement uncertainty for a given measurand. Uncertainty is defined with a coverage factor of 2 and has been calculated in line with the rules of the Guide to the Expression of Uncertainty in Measurement (GUM), taking into account environmental conditions, aging, wear and tear.

Device settings and GUI parameters are indicated as follows: “parameter: value”.

Typical data as well as nominal and measured values are not warranted by Rohde & Schwarz.

Base unit

Vertical system

Input channels	R&S®RTO2002	2 channels
	R&S®RTO2004	4 channels
	R&S®RTO2012	2 channels
	R&S®RTO2014	4 channels
	R&S®RTO2022	2 channels
	R&S®RTO2024	4 channels
	R&S®RTO2032	2 channels
	R&S®RTO2034	4 channels
	R&S®RTO2044	4 channels
Input impedance		50 Ω ± 3.5 % (50 Ω ± 1.5 % from +15 °C to +30 °C), 1 MΩ ± 1 % || 15 pF (meas.)
Analog bandwidth (–3 dB)	at 50 Ω input impedance	
	R&S®RTO2002 and R&S®RTO2004	≥ 600 MHz
	R&S®RTO2012 and R&S®RTO2014	≥ 1 GHz
	R&S®RTO2022 and R&S®RTO2024	≥ 2 GHz
	R&S®RTO2032 and R&S®RTO2034	≥ 3 GHz
	R&S®RTO2044	≥ 4 GHz
	at 1 MΩ input impedance	≥ 500 MHz (meas.)
Analog bandwidth limits	max. –1.5 dB, min. –4 dB	200 MHz, 20 MHz
Rise time/fall time	10 % to 90 % at 50 Ω (calculated)	
	R&S®RTO2002 and R&S®RTO2004	583 ps
	R&S®RTO2012 and R&S®RTO2014	350 ps
	R&S®RTO2022 and R&S®RTO2024	175 ps
	R&S®RTO2032 and R&S®RTO2034	116 ps
	R&S®RTO2044	100 ps
Input VSWR	input frequency ≤ 2 GHz	1.25 (meas.)
	input frequency > 2 GHz	1.4 (meas.)
Vertical resolution		8 bit, 16 bit for high resolution decimation (with reduction of the sampling rate), 16 bit for high definition mode (without reduction of the sampling rate, requires the option R&S®RTO-K17)
Effective number of bits of digitizer	for full-scale sine-wave signal with frequency equal to or lower than –3 dB bandwidth	> 7.0 bit (meas.)
DC gain accuracy	offset and position set to 0 V, after self-alignment	
	at 50 Ω, input sensitivity > 5 mV/div	±1.5 %
	at 50 Ω, input sensitivity ≤ 5 mV/div	±2 %
	at 1 MΩ	±2 %
Input coupling	at 50 Ω	DC and GND
	at 1 MΩ	DC, AC and GND
Input sensitivity	at 50 Ω	1 mV/div to 1 V/div, entire analog bandwidth supported for all input sensitivities
	at 1 MΩ	1 mV/div to 10 V/div, entire analog bandwidth supported for all input sensitivities
Maximum input voltage	at 50 Ω	5 V (RMS)
	at 1 MΩ	150 V (RMS), 200 V (V_p), derates at 20 dB/decade to 5 V (RMS) above 250 kHz
Position range		±5 div
Offset range at 50 Ω	input sensitivity	
	316 mV/div to ≤ 1 V/div	±10 V
	100 mV/div to ≤ 316 mV/div	±3 V
	1 mV/div to ≤ 100 mV/div	±1 V

Offset range at 1 MΩ	input sensitivity		
	3.16 V/div to ≤ 10 V/div	±(115 V – input sensitivity × 5 div)	
	1 V/div to ≤ 3.16 V/div	±100 V	
	316 mV/div to ≤ 1 V/div	±(11.5 V – input sensitivity × 5 div)	
	100 mV/div to ≤ 316 mV/div	±10 V	
	31.6 mV/div to ≤ 100 mV/div	±(1.15 V – input sensitivity × 5 div)	
	1 mV/div to ≤ 31.6 mV/div	±1 V	
Offset accuracy		±(0.35 % × |net offset| + 2.5 mV + 0.1 div × input sensitivity) (net offset = offset – position × input sensitivity)	
DC measurement accuracy	after adequate suppression of measurement noise using high-resolution sampling mode or waveform averaging or a combination of both	±(DC gain accuracy × |reading – net offset| + offset accuracy)	
Channel-to-channel isolation (each channel at same input sensitivity)	input frequency ≤ 2 GHz	> 60 dB	
	input frequency > 2 GHz	> 50 dB	
RMS noise floor at 50 Ω (typ.)	input sensitivity	R&S®RTO2002, R&S®RTO2004	R&S®RTO2012, R&S®RTO2014
	1 mV/div	0.08 mV	0.10 mV
	2 mV/div	0.08 mV	0.10 mV
	5 mV/div	0.11 mV	0.12 mV
	10 mV/div	0.17 mV	0.20 mV
	20 mV/div	0.28 mV	0.36 mV
	50 mV/div	0.70 mV	0.85 mV
	100 mV/div	1.30 mV	1.65 mV
	200 mV/div	2.70 mV	3.30 mV
	500 mV/div	7.00 mV	8.70 mV
	1 V/div	13.7 mV	17.0 mV
	input sensitivity	R&S®RTO2022, R&S®RTO2024	R&S®RTO2032, R&S®RTO2034
	1 mV/div	0.15 mV	0.19 mV
	2 mV/div	0.15 mV	0.19 mV
	5 mV/div	0.18 mV	0.24 mV
	10 mV/div	0.28 mV	0.37 mV
	20 mV/div	0.50 mV	0.67 mV
	50 mV/div	1.22 mV	1.70 mV
	100 mV/div	2.39 mV	3.30 mV
	200 mV/div	4.80 mV	6.60 mV
	500 mV/div	12.0 mV	16.6 mV
	1 V/div	23.9 mV	32.9 mV
	input sensitivity	R&S®RTO2044 (meas.)	
	1 mV/div	0.24 mV	
	2 mV/div	0.25 mV	
	5 mV/div	0.28 mV	
	10 mV/div	0.42 mV	
	20 mV/div	0.72 mV	
	50 mV/div	1.80 mV	
	100 mV/div	3.60 mV	
	200 mV/div	7.20 mV	
	500 mV/div	18.0 mV	
	1 V/div	36.0 mV	

Horizontal system

Timebase range		selectable between 25 ps/div and 10 000 s/div, time per div settable to any value within range
Channel deskew		±100 ns
Reference position		10 % to 90 % of measurement display area
Trigger offset range	max.	+(memory depth/current sampling rate)
	min.	–10 000 s
Modes		normal, roll

Channel-to-channel skew		< 100 ps (meas.)
Timebase accuracy	standard	
	after delivery/calibration, at +23 °C	±5 ppm
	during calibration interval	±10 ppm
	with R&S®RTO-B4 option	
	after delivery/calibration, at +23 °C	±0.02 ppm
	during calibration interval	±0.2 ppm
	long-term stability (more than one year since calibration)	±(0.1 + 0.1 × years since calibration) ppm
Delta time accuracy	corresponds to time error between two edges on same acquisition and channel; signal amplitude greater than 5 divisions, measurement threshold set to 50 %, vertical gain 10 mV/div or greater; rise time lower than four sample periods; waveform acquired in realtime mode	±(K/realtime sampling rate + timebase accuracy × |reading|) (peak) (meas.) where K = 0.15 (R&S®RTO2002, R&S®RTO2004) K = 0.18 (R&S®RTO2012, R&S®RTO2014) K = 0.25 (R&S®RTO2022, R&S®RTO2024) K = 0.37 (R&S®RTO2032, R&S®RTO2034) K = 0.43 (R&S®RTO2044)

Acquisition system

Realtime sampling rate	R&S®RTO2002, R&S®RTO2004, R&S®RTO2012, R&S®RTO2014, R&S®RTO2022, R&S®RTO2024, R&S®RTO2032, R&S®RTO2034,	max. 10 Gsample/s on each channel
	R&S®RTO2044	max. 10 Gsample/s on 4 channels, max. 20 Gsample/s on 2 channels
Realtime waveform acquisition rate	max.	> 1 000 000 waveforms/s
Memory depth [1]	standard	
	R&S®RTO2002, R&S®RTO2012, R&S®RTO2022, R&S®RTO2032	50 Msample on 2 channels, 100 Msample on 1 channel
	R&S®RTO2004, R&S®RTO2014, R&S®RTO2024, R&S®RTO2034, R&S®RTO2044	50 Msample on 4 channels, 100 Msample on 2 channels, 200 Msample on 1 channel
	R&S®RTO-B102 option	
	R&S®RTO2002, R&S®RTO2012, R&S®RTO2022, R&S®RTO2032	100 Msample on 2 channels, 200 Msample on 1 channel
	R&S®RTO2004, R&S®RTO2014, R&S®RTO2024, R&S®RTO2034, R&S®RTO2044	100 Msample on 4 channels, 200 Msample on 2 channels, 400 Msample on 1 channel
	R&S®RTO-B103 option	
	R&S®RTO2002, R&S®RTO2012, R&S®RTO2022, R&S®RTO2032	200 Msample on 2 channels, 400 Msample on 1 channel
	R&S®RTO2004, R&S®RTO2014, R&S®RTO2024, R&S®RTO2034, R&S®RTO2044	200 Msample on 4 channels, 400 Msample on 2 channels, 800 Msample on 1 channel
	R&S®RTO-B104 option	
	R&S®RTO2002, R&S®RTO2012, R&S®RTO2022, R&S®RTO2032	400 Msample on 2 channels, 800 Msample on 1 channel
	R&S®RTO2004, R&S®RTO2014, R&S®RTO2024, R&S®RTO2034, R&S®RTO2044	400 Msample on 4 channels, 800 Msample on 2 channels (restriction: 400 Msample on 2 channels when Ch1 and Ch2 or Ch3 and Ch4 are turned on), 800 Msample on 1 channel
	R&S®RTO-B110 option	
	R&S®RTO2002, R&S®RTO2012, R&S®RTO2022, R&S®RTO2032	1 Gsample on 2 channels, 2 Gsample on 1 channel
	R&S®RTO2004, R&S®RTO2014, R&S®RTO2024, R&S®RTO2034, R&S®RTO2044	1 Gsample on 4 channels, 2 Gsample on 2 channels (restriction: 1 Gsample on 2 channels when Ch1 and Ch2 or Ch3 and Ch4 are turned on), 2 Gsample on 1 channel

[1] The maximum available memory depth depends on the bit depth of the acquired data and, therefore, on the settings of the acquisition system, such as decimation mode, waveform arithmetic, number of waveform streams and high definition mode.

Decimation modes	sample	first sample in decimation interval
	peak detect	largest and smallest sample in decimation interval
	high resolution	average value of samples in decimation interval
	root mean square	root of squared average of samples in decimation interval
Waveform arithmetic	off	no arithmetic
	envelope	envelope of acquired waveforms
	average	average of acquired waveforms, max. average depth depends on decimation mode [2]
	sample	max. 16 777 215
	high resolution	max. 65 535
	root mean square	max. 255
	reset condition	no reset (standard), reset by time, reset by number of processed waveforms
Waveform streams per channel		up to 3 with independent selection of decimation mode and waveform arithmetic
Sampling modes	realtime mode	max. sampling rate set by digitizer
	interpolated time	enhancement of sampling resolution by interpolation; max. equivalent sampling rate is 4 Tsample/s
Interpolation modes		linear, sin(x)/x, sample&hold
Ultra segmented mode		continuous recording of waveforms in acquisition memory without interruption due to visualization; blind time between consecutive acquisitions less than 300 ns

Trigger system

Sources	R&S®RTO2002, R&S®RTO2012, R&S®RTO2022, R&S®RTO2032	channel 1, channel 2
	R&S®RTO2004, R&S®RTO2014, R&S®RTO2024, R&S®RTO2034, R&S®RTO2044	channel 1, channel 2, channel 3, channel 4
Sensitivity	trigger hysteresis mode	auto (standard) or manual
	range	0 V to 5 div × input sensitivity
Trigger jitter	full-scale sine wave of frequency set to –3 dB bandwidth	< 1 ps (RMS) (meas.)
Coupling mode	standard	same as selected channel
	lowpass filter	cutoff frequency selectable from 100 kHz to 50 % of analog bandwidth
Sweep mode		auto, normal, single, n single
Event rate	max.	one event for every 400 ps time interval
Trigger level	range	±5 div from center of screen
Holdoff range	time	100 ns to 10 s, fixed and random
	events	1 event to 2 000 000 000 events

Main trigger modes		
Edge	triggers on specified slope (positive, negative or either) and level	
Glitch	triggers on glitches of positive, negative or either polarity that are shorter or longer than specified width	
	glitch width	100 ps to 1000 s
		50 ps to 1000 s (R&S®RTO2044 only)
Width	triggers on positive or negative pulse of specified width; width can be shorter, longer, inside or outside the interval	
	pulse width	100 ps to 1000 s
		50 ps to 1000 s (R&S®RTO2044 only)

[2] Waveform averaging is not compatible with peak detect decimation.

Runt	triggers on pulse of positive, negative or either polarity that crosses one threshold but fails to cross a second threshold before crossing the first one again; runt pulse width can be arbitrary, shorter, longer, inside or outside the interval	
	runt pulse width	100 ps to 1000 s
		50 ps to 1000 s (R&S®RTO2044 only)
Window	triggers when signal enters or exits a specified voltage range; triggers also when signal stays inside or outside the voltage range for a specified period of time	
Timeout	triggers when signal stays high, low or unchanged for a specified period of time	
	timeout	100 ps to 1000 s
		50 ps to 1000 s (R&S®RTO2044 only)
Interval	triggers when time between two consecutive edges of same slope (positive or negative) is shorter, longer, inside or outside a specified range	
	interval time	100 ps to 1000 s
		50 ps to 1000 s (R&S®RTO2044 only)
Slew rate	triggers when the time required by a signal edge to toggle between user-defined upper and lower voltage levels is shorter, longer, inside or outside the interval; edge slope may be positive, negative or either	
	toggle time	100 ps to 1000 s
		50 ps to 1000 s (R&S®RTO2044 only)
Data2clock	triggers on setup time and hold time violations between clock and data present on any two input channels; monitored time interval may be specified by the user in the range from –100 ns to 100 ns around a clock edge and must be at least 100 ps wide	
Pattern	triggers when a logical combination (and, nand, or, nor) of the input channels stays true for a period of time shorter, longer, inside or outside a specified range	
State	triggers when a logical combination (and, nand, or, nor) of the input channels stays true at a slope (positive, negative or either) in one selected channel	
Serial pattern	triggers on serial data pattern up to 128 bit clocked by one input channel; pattern bits may be high (H), low (L) or don't care (X); clock edge slope may be positive, negative or either; hardware CDR selectable as clock source (requires R&S®RTO-K13 option)	
	max. data rate	< 2.50 Gbps
		< 5 Gbps (R&S®RTO2044 only)
TV/video	triggers on baseband analog progressive and interlaced video signals including NTSC, PAL, PAL-M, SECAM, EDTV and HDTV broadcast standards as well as custom bi-level and tri-level sync video standards	
	trigger modes	all fields, odd fields, even fields, all lines, line number

Advanced trigger modes		
Trigger qualification	trigger events may be qualified by a logical combination of unused channels	
	qualifiable events	edge, glitch, width, runt, window, timeout, interval
Sequence trigger (A/B/R trigger)	triggers on B event after occurrence of A event; delay condition after A event specified either as time interval or number of B events; an optional R event resets the trigger sequence to A	
	A event	any trigger mode
	B event	edge, glitch, width, runt, window, timeout, interval, slew rate
	R event	edge, glitch, width, runt, window, timeout, interval, slew rate
Zone trigger		with R&S®RTO-K19 option
Serial bus trigger	basic	I^2C, SPI, UART/RS-232
	optional	LIN, CAN, FlexRay™, I^2S, MIL-STD-1553, ARINC 429, CAN FD, SENT, MIPI RFFE, Manchester, NRZ, MDIO and USB 1.0/1.1/2.0/HSIC with dedicated software options
NFC trigger		with R&S®RTO-K11 option

CDR trigger	triggers on clock signal recovered from the trigger source signal; phase of the trigger instant user-selectable as fraction of bit period; requires R&S®RTO-K13 option	
	CDR configuration parameters	PLL order (first or second), nominal bit rate, loop bandwidth, relative bandwidth, damping factor, unit interval offset
	CDR bit rate range	
	R&S®RTO2002, R&S®RTO2004, R&S®RTO2012, R&S®RTO2014, R&S®RTO2022, R&S®RTO2024	200 kbps to 2.5 Gbps
	R&S®RTO2044	200 kbps to 2.5 Gpbs standard, 400 kbps to 5.0 Gbps when operating at 20 Gsample/s realtime sampling rate [3]
External trigger input	input impedance	50 Ω ± 1.5 % or 1 MΩ ± 1 % || 20 pF (meas.)
	max. input voltage at 50 Ω	5 V (RMS)
	max. input voltage at 1 MΩ	30 V (RMS) derates at 20 dB/decade to 5 V (RMS) above 25 MHz
	trigger level	±5 V
	sensitivity	
	input frequency ≤ 100 MHz	300 mV (V_{pp})
	100 MHz < input frequency ≤ 500 MHz	600 mV (V_{pp})
	input coupling	AC, DC (50 Ω and 1 MΩ), GND, HF reject (attenuates > 50 kHz or > 50 MHz, user-selectable), LF reject (attenuates < 5 kHz or < 50 kHz, user-selectable)
	trigger modes	edge (rise or fall)
Trigger out	functionality	a pulse is generated for every acquisition trigger event
	output voltage	0 V to 5 V at high impedance; 0 V to 2.5 V at 50 Ω
	pulse width	selectable between 50 ns and 60 ms
	pulse polarity	low active or high active
	output delay	depends on trigger settings
	jitter	±600 ps (meas.)

[3] The front-end of the R&S®RTO2044 samples at 20 Gsample/s when at most one channel from each pair {channel1, channel2} and {channel3, channel4} is active; and the user-selected sampling resolution in realtime sampling mode or interpolated time sampling mode is 50 ps or smaller.

Waveform measurements

General features	measurement panels	up to 8 measurement panels; each panel may contain any number of automatic measurements of the same category
	gate	delimits the display region evaluated for automatic measurements
	reference levels	user-configurable vertical levels define support structures for automatic measurements
	statistics	displays maximum, minimum, mean, standard deviation, RMS and measurement count for each automatic measurement
	track	measurement results displayed as continuous trace that is time-correlated to the measurement source; requires R&S®RTO-K12 or R&S®RTO-K31 option
	long-term analysis	history of selected measurements as trace against count index
	histogram	available for the main measurement of each measurement panel; automatic or manual selection of bin number and scale; counters for measurements under, within and over the histogram range
	limit check	measurements tested against user-defined margins and limits; pass or fail conditions may launch automatic response: acquisition stop, beep, print and save waveform
Measurement category	amplitude and time	amplitude, high, low, maximum, minimum, peak-to-peak, mean, RMS, sigma, overshoot, area, rise time, fall time, positive width, negative width, period, frequency, duty cycle, delay, phase, burst width, pulse count, positive switching, negative switching, cycle area, cycle mean, cycle RMS, cycle sigma, setup/hold time, setup/hold ratio, pulse train, DC voltmeter (requires Rohde & Schwarz active probe with R&S®ProbeMeter functionality)
	eye diagram	extinction ratio, eye height, eye width, eye top, eye base, Q factor, S/N ratio, duty cycle distortion, eye rise time, eye fall time, eye bit rate, eye amplitude, jitter (peak-to-peak, 6-sigma, RMS)
	spectrum	channel power, bandwidth, occupied bandwidth, total harmonic distortion
	jitter	cycle-to-cycle jitter, N-cycle jitter, cycle-to-cycle width, cycle-to-cycle duty cycle, time-interval error, data rate, unit interval, skew delay, skew phase; requires R&S®RTO-K12 option
Cursors	setup	up to 4 cursor sets on screen, each set consisting of two horizontal and two vertical cursors
	target	acquired waveforms (input channels), math waveforms, reference waveforms, track waveforms, XY diagrams
	operating mode	vertical measurements, horizontal measurements or both; vertical cursors either set manually or locked to waveform

Histogram	source	acquired waveform (input channels), math waveform, reference waveform
	mode	vertical (for timing statistics), horizontal (for amplitude statistics)
	automatic measurements	waveform count, waveform samples, histogram samples, histogram peak, peak value, maximum, minimum, median, range, mean, sigma, mean ± 1, 2 and 3 sigma, marker ± probability

Mask testing

Test definition	number of masks	up to 8 simultaneously
	source	acquired waveforms (input channels), math waveforms
	fail condition	sample hit or waveform hit
	fail tolerance	minimum number of fail events for test fail in range from 0 to 4 000 000 000
	test rate	up to 600 000 waveforms per second
	action on error	acquisition stop, beep, print and save waveform
	save/load to file	test and mask settings (.xml format)
Mask definition with segments	number of independent segments	up to 8
	segment definition	array of points and connecting rule (upper, lower, inner) define segment region
	segment input	point and click on touchscreen, editable list
Mask definition with tolerance tube	input signal	acquired waveform
	definition of tolerance tube	horizontal width, vertical width, vertical stretch, vertical position
Mask definition with eye mask assistant (requires R&S®RTO-K12 option)	primary mask shape	
	type	diamond, square, hexagon, octagon
	dimensions	main and secondary height, main and secondary width, depending on selected shape
	position	vertical offset, horizontal offset
	secondary mask shapes	
	locations	any combination of left, right, top, bottom
	position	horizontal and vertical offset with respect to center of primary mask shape
Result statistics	category	completed acquisitions, remaining acquisitions, state, sample hits, mask hits, fail rate, test result (pass or fail)
Visualization options	waveform style	vectors, dots
	violation highlighting	hits (on/off), highlight persistence (50 ms to 50 s or infinite), waveform color (default: red)
	mask colors	configurable colors for mask without violation (default: translucent gray), mask with violation (default: translucent red), mask with contact (default: translucent pale red)

Waveform math

General features	number of math waveforms	up to 4
	number of reference waveforms	up to 4
	waveform arithmetic	user-selectable average or envelope of consecutive waveforms
Algebraic expressions	user may define complex mathematical expressions involving waveforms and measurement results	
	math functions	add, subtract, multiply, divide, absolute value, square, square root, integrate, differentiate, exp, $\log_{10}$, $\log_e$, $\log_2$, rescale, sin, cos, tan, arcsin, arccos, arctan, sinh, cosh, tanh, autocorrelation, crosscorrelation
	logical operators	not, and, nand, or, nor, xor, nxor
	relational operators	Boolean result of =, ≠, >, <, ≤, ≥
	frequency domain	spectral magnitude and phase, real and imaginary spectra, group delay
	digital filter	lowpass, highpass
	special functions	CDR transform; requires R&S®RTO-K12 option
Optimized math	operators	add, subtract, multiply, invert, absolute value, differentiate, $\log_{10}$, $\log_e$, $\log_2$, rescale, FIR, FFT magnitude
Spectrum analysis	FFT magnitude spectrum	
	setup parameters	center frequency, frequency span, frame overlap, frame window (rectangular, Hamming, Hann, Blackman, Gaussian, Flattop, Kaiser Bessel), user-selectable spectrum averaging and envelope

Search and mark function

General description	scans acquired waveforms for occurrence of a user-defined set of events and highlights each occurrence	
Basic setup	source	all physical input channels, math waveforms, reference waveforms
	search panels	up to 8, where each panel may manage multiple event searches
	search mode	manually triggered or continuous
	search conditions	
	supported events	edge, glitch, width, runt, window, timeout, interval, slew rate, data2clock, state
	event configuration	identical to corresponding trigger event
	event selection	single or multiple events on same source
Search scope	mode	current waveform, gated time interval
Result visualization	table	
	sort mode	horizontal position or vertical value
	max. result count	specifies max. table size
	zoom window	centered on highlighted event

Display characteristics

Diagram types	Yt, XY, spectrum, long-term measurement
Display interface configuration	display area can be split up into separate diagram areas by dragging and dropping signal icons; each diagram area can hold any number of signals; diagram areas may be stacked on top of each other and later accessed via the dynamic tab menu
Signal bar	accommodates timebase settings, trigger settings and signal icons; signal bar may be docked to left or right side of display area or hidden
Signal icon	each active waveform is represented by a separate signal icon on the signal bar; the signal icon displays the individual vertical and acquisition settings; a waveform can be minimized to its signal icon so that it appears as a realtime preview in miniature form; dialog boxes and measurement results may also be minimized to a signal icon
Axis label	X-axis ticks and Y-axis ticks labeled with tick value and physical unit
Diagram label	diagrams may be individually labeled with a descriptive user-defined name
Diagram layout	grid, crosshair, axis labels and diagram label may be switched on and off separately
Persistence	50 ms to 50 s, or infinite
Zoom	user-defined zoom window provides vertical and horizontal zoom; each diagram area supports multiple zoom windows; touchscreen interface simplifies resize and drag operations on zoom window
Signal colors	predefined or user-defined color tables for persistence display

Input and output

Front		
Channel inputs		BNC-compatible, for details see “Vertical system”
	probe interface	auto-detection of passive probes, Rohde & Schwarz active probe interface
Auxiliary output		SMA connector, for future use
Probe compensation output	signal shape	rectangle, V_{low} = 0 V, V_{high} = 1 V amplitude 1 V (V_{pp}) ± 5 %
	frequency	1 kHz ± 1 %
	impedance	50 Ω (nom.)
Ground jack		connected to ground
USB interface		2 ports, type A plug, version 2.0

Rear		
External trigger input		BNC, for details see “Trigger system”
Trigger out		BNC, for details see “Trigger system”
USB interface		2 ports, type A plug and 1 port, type B plug, version 3.1 gen 1
LAN interface		RJ-45 connector, supports 10/100/1000BASE-T
External monitor interface		DVI-D and DisplayPort, output of scope display or extended desktop display
GPIB interface		see R&S®RTO-B10 option
Reference input		see R&S®RTO-B4 option
Reference output		see R&S®RTO-B4 option
Security slot		for standard Kensington style lock

General data

Display	type	12.1" LC TFT color display with capacitive touchscreen
	resolution	1280 × 800 pixel (WXGA)

Temperature		
Temperature loading	operating temperature range	0 °C to +45 °C
	storage temperature range	–40 °C to +70 °C
Temperature loading		in line with MIL-PRF-28800F section 4.5.5.1.1.1 class 3 tailored to +45 °C for operation
Climatic loading		+25° C/+40 °C at 85 % rel. humidity cyclic, in line with IEC 60068-2-30
		+30 °C/+40 °C/+45 °C at 95/75/45 % in line with MIL-PRF-28800F section 4.5.5.1.1.1 class 3 tailored to +45 °C for operation

Altitude		
Operating		up to 3000 m above sea level
Nonoperating		up to 4600 m above sea level

Mechanical resistance		
Vibration	sinusoidal	5 Hz to 150 Hz, max. 2 g at 55 Hz; 0.5 g from 55 Hz to 150 Hz; in line with EN 60068-2-6
		5 Hz to 55 Hz, in line with MIL-PRF-28800F section 4.5.5.3.2 class 3
	random	10 Hz to 300 Hz, acceleration 1.2 g (RMS), in line with EN 60068-2-64
		5 Hz to 500 Hz, acceleration 2.058 g (RMS), in line with MIL-PRF-28800F section 4.5.5.3.1 class 3
Shock		40 g shock spectrum, in line with MIL-STD-810E, method no. 516.4, procedure I
		30 g functional shock, halfsine, duration 11 ms, in line with MIL-PRF-28800F section 4.5.5.4.1

EMC		
RF emission	in line with EN 55011 class A, operation in residential, commercial and business areas or in small-size companies is not covered; therefore the instrument may not be operated in residential, commercial and business areas or in small-size companies unless additional measures are taken to ensure that EN 55011 class B is complied with	in line with CISPR 11/EN 55011 group 1 class A (for a shielded test setup); the instrument complies with the emission requirements stipulated by EN 55011, EN 61326-1 and EN 61326-2-1 class A, making the instrument suitable for use in industrial environments
Immunity		in line with IEC/EN 61326-1 table 2, immunity test requirements for industrial environment [4]

Certifications		VDE-GS, $_{C}$CSA$_{US}$, KC

[4] Test criterion is displayed noise level within ±1 div for input sensitivity of 5 mV/div.

Calibration interval		1 year

Power supply		
AC supply		100 V to 240 V at 50 Hz to 60 Hz and 400 Hz, max. 5.5 A to 2.3 A, in line with MIL-PRF 28800F section 3.5
Power consumption		max. 450 W
Safety		in line with IEC 61010-1, EN 61010-1, CAN/CSA-C22.2 No. 61010-1-04, UL 61010-1

Mechanical data		
Dimensions	W × H × D	427 mm × 249 mm × 204 mm (16.81 in × 9.80 in × 8.03 in)
Weight	without options, nominal	9.6 kg (21.16 lb)

Warranty	base unit	3 years
	accessories	1 year

Options

R&S®RTO-B1

Mixed signal option, additional 16 logic channels

Vertical system

Input channels		16 logic channels (D0 to D15)
Arrangement of input channels		arranged in two logic probes with 8 channels each, assignment of the logic probes to the channels (D0 to D7 or D8 to D15) is displayed on the probe
Input impedance		100 kΩ ± 2 % || ~4 pF (meas.) at probe tips
Maximum input frequency	signal with minimum input voltage swing and hysteresis setting: normal	400 MHz (meas.)
Maximum input voltage		±40 V (V_p)
Minimum input voltage swing		500 mV (V_{pp}) (meas.)
Threshold groups		D0 to D3, D4 to D7, D8 to D11 and D12 to D15
Threshold level	range	±8 V in 25 mV steps
	predefined	CMOS 5.0 V, CMOS 3.3 V, CMOS 2.5 V, TTL, ECL, PECL, LVPECL
Threshold accuracy		±(100 mV + 3 % of threshold setting)
Comparator hysteresis		normal, robust, maximum

Horizontal system

Channel deskew	range for each channel	±200 ns
Channel-to-channel skew		< 500 ps (meas.)

Acquisition system

Sampling rate	max.	5 Gsample/s on each channel
Realtime waveform acquisition rate	max.	> 200 000 waveforms/s
Memory depth	at max. sampling rates	200 Msample for every channel
	at lower sampling rates	100 Msample for every channel
Decimation		pulses lost due to decimation are displayed

Trigger system

Holdoff range	time	100 ns to 10 s, fixed and random
	events	1 event to 2 000 000 000 events

Trigger modes		
Edge	triggers on specified slope (positive, negative or either) in the source signal	
	sources	any channel from D0 to D15 or any logical combination of D0 to D15
Width	triggers on positive or negative pulse of specified width in the source signal; width can be shorter, longer, equal, inside or outside the interval	
	sources	any channel from D0 to D15 or any logical combination of D0 to D15
	pulse width	200 ps to 10 s
Timeout	triggers when the source signal stays high, low or unchanged for a specified period of time	
	sources	any channel from D0 to D15 or any logical combination of D0 to D15
	timeout	200 ps to 10 s
Data2clock	triggers on setup time and hold time violations between a clock signal and a data signal; monitored time interval with a max. width of 200 ns and a position of max. ±1 µs relative to the clock edge	
	data signal	any subset of channels from D0 to D15 or any user-defined bus signal
	clock signal	any channel from D0 to D15

Pattern	triggers when the source goes true or stays true for a period of time shorter, longer, equal, inside or outside a specified range	
	sources	any logical combination of D0 to D15 or any user-defined bus signal
	pulse width	200 ps to 10 s
State	triggers on the slope (positive, negative or either) of the clock signal when data signal matches a user-defined logical state	
	data signal	any logical combination of D0 to D15 or any user-defined bus signal
	clock signal	any channel from D0 to D15
Serial pattern	triggers on a serial data pattern of up to 32 bit; pattern bits may be high (H), low (L) or don't care (X); clock edge slope may be positive, negative or either	
	data signal	any channel from D0 to D15 or any logical combination of D15 to D15
	clock signal	any channel from D0 to D15
	max. data rate	1 Gbps
Serial bus trigger	basic	I^2C, SPI, UART/RS-232
	optional	LIN, CAN, FlexRay™ and I^2S with dedicated software options
	sources	any channel from D0 to D15

Waveform measurements

General features		measurement panels, gate, statistics, long-term analysis and limit check; see features of the base unit
Measurement sources		all channels from D0 to D15 or any logical combination of D0 to D15
Automatic measurements		positive pulse width, negative pulse width, period, frequency, burst width, delay, phase, positive duty cycle, negative duty cycle, positive pulse count, negative pulse count, rising edge count, falling edge count
Additional cursor function		display of decoded bus value at the cursor position

Waveform math

Function		any logical combination of D0 to D15

Search and mark functions

The search function will be available in a future software release.

Display characteristics

Display of logical channels		selectable size and position on screen, diagram configuration by dragging and dropping signal icons
Bus decode	number of bus signals	4
	bus types	unclocked and clocked
	display types	decoded bus, logical signal, bus + logical signal, amplitude signal, amplitude + logical signal, tabulated list (decoded time interval selected with cursors)
	position and size	size and position on screen selectable
	data format of decoded bus	hex, unsigned integer, signed integer, fractional, binary
	data format of amplitude signal	unsigned integer, signed integer, fractional, binary offset
Channel activity display		independent of the scope acquisition, the state (stays low, stays high or toggles) of the channels from D0 to D15 is displayed in the signal icon

R&S®RTO-B4

OCXO, precision reference frequency with reference input and output connectors		
Timebase accuracy	OCXO	see “Horizontal system”
Reference output	connector	BNC female
	impedance	50 Ω (nom.)
	output frequency with OCXO	10 MHz (nom.)
	output frequency with auxiliary reference	same as auxiliary reference
	level	> 7 dBm
Auxiliary reference input	connector	BNC female
	impedance	50 Ω (nom.)
	input frequency range	1 MHz ≤ f_{in} ≤ 20 MHz, in 1 MHz steps
	required level	≥ 0 dBm into 50 Ω

R&S®RTO-B10

Additional GPIB interface		
Function		interface in line with IEC 625-2 (IEEE 488.2)
Command set		SCPI 1999.0
Connector		24-pin Amphenol female
Interface functions		SH1, AH1, T6, L4, SR1, RL1, PP1, DC1, DT1, C0

R&S®RTO-B19

Additional solid state disk		
Disk type		solid state disk
Disk size		≥ 240 Gbyte (nom.)
Firmware		Is installed upon delivery.

R&S®RTO-K1

I²C decoding		
Protocol configuration	bit rate	up to 3.4 Mbps (auto-detected)
	auto threshold setup	assisted threshold configuration for I²C triggering and decoding
	device list	associate frame address with symbolic ID
Trigger (included in standard equipment)	source (clock and data)	any input channel or logical channel
	trigger event setup	start, stop, restart, missing ACK, address, data, address + data
	address setup	7 bit or 10 bit address (value in hex, decimal, octal or binary); ACK, NACK or either; read, write or either; R/W bit included in address value or apart; condition =, ≠, ≥, ≤, in range, out of range
	data setup	data pattern up to 8 byte (hex, decimal, octal or binary); condition =, ≠; ≥, ≤, in range, out of range; offset within frame in range from 0 byte to 4095 byte
Decode	source (clock and data)	any input channel, math waveform, reference waveform, logical channel
	display type	decoded bus, logical signal, bus + logical signal, tabulated list
	color coding	frame, start/restart, address, R/W bit, data, ACK/NACK, stop, error
	address and data format	hex, decimal, octal, binary, ASCII; symbolic names for user-defined subset of addresses
Search	search event setup	combination of start, stop, restart, missing ACK, address, data, address + data
	event settings	same as trigger event settings

SPI decoding		
Protocol configuration	type	2-wire, 3-wire and 4-wire SPI
	bit rate	auto-detected
	bit order	LSB first, MSB first
	word size	4 bit to 32 bit
	frame condition	SS, timeout
	polarity (MOSI, MISO, SS, CLK)	active high, active low
	phase (CLK)	first edge, second edge
	auto threshold setup	assisted threshold configuration for SPI triggering and decoding
Trigger (included in standard equipment)	source (MOSI, MISO, SS, CLK)	any input channel or logical channel
	bit rate	up to 50 Mbps
	trigger event setup	start of frame, MOSI, MISO, MOSI + MISO
	data setup	data pattern up to 256 bit (hex or binary); condition =, ≠; offset within frame in range from 0 bit to 32767 bit
Decode	source (MOSI, MISO, SS, CLK)	any input channel, math waveform, reference waveform, logical channel
	display type	decoded bus, logical signal, bus + logical signal, tabulated list
	color coding	frame, word, error
	data format	hex, decimal, octal, binary, ASCII

R&S®RTO-K2

UART/RS-232/RS-422/RS-485 decoding		
Protocol configuration	bit rate	300 bps to 20 Mbps
	signal polarity	idle low, idle high
	number of bits	5 bit to 8 bit
	bit order	LSB first, MSB first
	parity	odd, even, mark, space, none
	stop bit	1, 1.5 or 2 bit periods
	end of packet	word, timeout, none
	auto threshold setup	assisted threshold configuration for UART triggering and decoding
Trigger (included in standard equipment)	source (TX and RX)	any input channel or logical channel
	trigger event setup	start bit, packet start, data, parity error, break condition
	data setup	data pattern up to 256 bit (hex, decimal, octal, binary or ASCII); condition =, ≠; offset within packet in range 0 bit to 32767 bit
Decode	source (TX and RX)	any input channel, math waveform, reference waveform, logical channel
	display type	decoded bus, logical signal, bus + logical signal, tabulated list
	color coding	packet, data payload, start error, parity error, stop error
	data format	hex, decimal, octal, binary, ASCII

R&S®RTO-K3

CAN triggering and decoding		
Protocol configuration	signal type	CAN_H, CAN_L
	bit rate	100 bps to 1 Mbps
	sampling point	5 % to 95 % within bit period
	device list	associate frame identifier with symbolic ID, load DBC file content
	auto threshold setup	assisted threshold configuration for CAN triggering and decoding
Trigger	source	any input channel or logical channel
	trigger event setup	start of frame, frame type, identifier, identifier + data, symbolic, error condition (any combination of CRC error, bit stuffing error, form error and ACK error)
	identifier setup	frame type (data, remote or both), identifier type (standard or extended); condition =, ≠, ≥, ≤, in range, out of range
	data setup	data pattern up to 8 byte (hex, decimal, octal or binary); big-endian or little-endian; condition =, ≠; ≥, ≤, in range, out of range
	symbolic setup	message name, signal name; numeric signal condition =, ≠, ≥, ≤, in range, out of range; enumerated signal condition =, ≠, ≥, ≤
Decode	source	any input channel, math waveform, reference waveform, logical channel
	display type	decoded bus, logical signal, bus + logical signal, tabulated list
	color coding	start of frame, identifier, DLC, data payload, CRC, end of frame, error frame, overload frame, CRC error, bit stuffing error
	data format	hex, decimal, octal, binary, ASCII, symbolic
Search	source	any input channel or logical channel
	search event setup	combination of start of frame, frame type, identifier, identifier + data, error condition (any combination of CRC error, bit stuffing error, form error and ACK error) or only symbolic
	event settings	same as trigger event settings

LIN triggering and decoding		
Protocol configuration	version	1.3, 2.x or SAE J602; mixed traffic is supported
	bit rate	standard bit rate (1.2/2.4/4.8/9.6/10.417/ 19.2 kbps) or user-defined bit rate in range from 1 kbps to 20 kbps
	device list	associate frame identifier with symbolic ID, data length and protocol version
	auto threshold setup	assisted threshold configuration for LIN triggering and decoding
Trigger	source	any input channel
	trigger event setup	start of frame (sync break), identifier, identifier + data, wakeup frame, error condition (any combination of checksum error, parity error and sync field error)
	identifier setup	range from 0d to 63d; select condition =, ≠, ≥, ≤, in range, out of range for trigger “identifier”; select single identifier and condition = for trigger “identifier + data”
	data setup	data pattern up to 8 byte (hex, decimal, octal or binary); condition =, ≠, ≥, ≤, in range, out of range

Decode	source (TX and RX)	any input channel, math waveform, reference waveform
	display type	decoded bus, logical signal, bus + logical signal, tabulated list
	color coding	frame, frame identifier, data payload, checksum, error condition
	data format	hex, decimal, octal, binary, ASCII
Search	search event setup	combination of start of frame (sync break), identifier, identifier + data, wakeup frame, error condition (any combination of checksum error, parity error and sync field error)
	event settings	same as trigger event settings

R&S®RTO-K4

FlexRay™ triggering and decoding		
Protocol configuration	signal type	single-ended, differential, logic
	channel type	channel A, channel B
	bit rate	standard bit rates (2.5/5.0/10.0 Mbps)
	device list	associate frame identifier with symbolic ID
	auto threshold setup	assisted threshold configuration for FlexRay™ triggering and decoding
	source	any input channel or logical channel
Trigger	trigger event setup	start of frame, header + data, symbol, wakeup, error condition (any combination of FSS error, BSS error, FES error, header CRC error and frame CRC error)
	header setup	indicator bits, identifier, payload length, cycle count
	indicator bits setup	payload preamble bit, null frame bit, sync frame bit and startup frame bit separately configurable (1, 0 or don't care)
	identifier setup	condition =, ≠, ≥, ≤, in range, out of range
	payload length setup	condition =, ≠, ≥, ≤, in range, out of range
	cycle count	condition =, ≠, ≥, ≤, in range, out of range; step parameter for selection of non-contiguous values within provided range
	data setup	data pattern up to 8 byte (hex, decimal, octal or binary); condition =, ≠, ≥, ≤, in range, out of range; offset within frame in range from 0 byte to 253 byte
Decode	source	any input channel, math waveform, reference waveform, logical channel
	display type	decoded bus, logical signal, bus + logical signal, tabulated list
	color coding	frame, frame header, identifier, payload length, header CRC, cycle count, data payload, frame CRC, error condition
	data format	hex, decimal, octal, binary, ASCII
Search	search event setup	combination of start of frame, header + data, symbol, wakeup, error condition (any combination of FSS error, BSS error, FES error, header CRC error and frame CRC error)
	event settings	same as trigger event settings

R&S®RTO-K5

I²S triggering and decoding		
Protocol configuration	signal type	I²S standard, left justified, right justified, TDM
	auto threshold setup	assisted threshold configuration for I²S triggering and decoding
Trigger	source	any input channel or logical channel
	trigger event setup	data, window, frame condition, word select, error condition
	data setup	data pattern of an audio channel up to 4 byte (hex, signed decimal, unsigned decimal, octal or binary); condition =, ≠; ≥, ≤, <, >, in range, out of range
	window setup	word count of data pattern of an audio channel up to 4 byte (hex, signed decimal, unsigned decimal, octal or binary); condition =, ≠; ≥, ≤, <, >, in range, out of range
	frame condition setup	combination of audio channels in a frame, up to 4 byte (hex, signed decimal, unsigned decimal, octal or binary); condition =, ≠; ≥, ≤, <, >, in range, out of range
	word select setup	rising or falling edge of word select input channel
	error condition setup	source of word select
Decode	source	any input channel, math waveform, reference waveform, logical channel
	display type	decoded bus, logical signal, bus and logical signal, tabulated list
	color coding	audio frame, frame error, incomplete frame
	data format	hex, unsigned decimal, signed decimal (two's complement), octal, binary, ASCII
Protocol measurements	audio display	display of audio waveform for specified audio channels
	long-term display	history of selected audio data as trace against measurements, waveforms and time index

R&S®RTO-K6

MIL-STD-1553 triggering and decoding		
Protocol configuration	signal type	single-ended
	bit rate	standard bit rate (1 Mbit/s)
	polarity	normal, inverted
	device list	associate frame identifier with symbolic ID
	auto threshold setup	assisted threshold configuration
	timing	min. gap (2 µs to 262 µs) or off; max. response (2 µs to 262 µs) or off
Trigger	trigger event setup	sync, word, data word, command/status word, command word, status word, error condition
	sync and word setup	all words, command/status word, data word
	data word setup	RTA (condition =, ≠, ≥, ≤, in range, out of range); data pattern (condition =, ≠, ≥, ≤, in range, out of range); payload data index (=, <, >, ≥, ≤, range); max length of data pattern is 4 byte
	command/status word setup	RTA (condition =, ≠, ≥, ≤, in range, out of range); 11 bit pattern (condition =, ≠, ≥, ≤, in range, out of range)
	command word setup	RTA (condition =, ≠, ≥, ≤, in range, out of range); subaddress/mode (condition =, ≠, ≥, ≤, in range, out of range); data word count/mode count (condition =, ≠, ≥, ≤, in range, out of range); direction (T/R)
	status word	RTA (condition =, ≠, ≥, ≤, in range, out of range); status flags (message error, instrumentation, service request, broadcast command, busy, subsystem flag, dynamic bus control, terminal flag)
	error condition	any combination of sync error, Manchester error, parity error, timing error (see protocol configuration)
Decode	source	any analog input channel, math waveform, reference waveform
	display type	decoded bus, logical signal, bus + logical signal, tabulated list
	color coding	frame (word), sync, RTA, status bit field, parity, data field, error condition
	data format	hex, octal, binary, ASCII, signed, unsigned
Search	search event setup	sync, word, data word, command/status word, command word, status word, error condition
	event settings	same as trigger event settings

R&S®RTO-K7

ARINC 429 triggering and decoding		
Protocol configuration	signal type	single-ended
	bit rate	high (100 kbit/s)
		low (12 kbit/s to 14.5 kbit/s)
	polarity	A leg, B leg
	device list	associate frame identifier with symbolic ID
	auto threshold setup	assisted threshold configuration
	timing	min. gap (0 bit to 100 bits) or off; max. gap (0 bit to 1000 bits) or off
Trigger	trigger event setup	word start, word stop, label + data, error condition
	label + data setup	label (condition =, ≠, ≥, ≤, in range, out of range); data (condition =, ≠, ≥, ≤, in range, out of range); SDI/SSM
	error condition	any combination of coding error, parity error, timing error (see protocol configuration)
Decode	source	any analog input channel, math waveform, reference waveform
	display type	decoded bus, logical signal, bus + logical signal, tabulated list
	color coding	frame (word), label, SDI, data, SSM, parity, error condition
	data format	hex, octal, binary, ASCII, signed, unsigned
Search	search event setup	word start, word stop, label + data, error condition
	event settings	same as trigger event settings

R&S®RTO-K8

Ethernet decoding		
Protocol configuration	signal type	one channel, differential
	bit rate	selectable/adjustable
	auto threshold setup	assisted threshold configuration
	source (SDATA)	analog and math channels
	variants	10BASE-T, 100BASE-TX
Decode	display type	decoded bus, logical signal, bus + logical signal, tabulated list, details
	color coding	preamble, frame, destination address, source address, data
	data format	hex
Search	search event setup	frame, error
	frame	48 bit destination address, 48 bit source address, 16 bit length/type, 32 bit frame check; conditions =, ≠, <, ≤, >, ≥, in range, out of range
	error	preamble, length error

R&S®RTO-K9

CAN-FD triggering and decoding		
Protocol configuration	signal type	CAN_H, CAN_L
	bit rate	
	arbitration rate	10 kbps to 1 Mbps
	data rate	10 kbps to 15 Mbps
	sampling point	5 % to 95 % within bit period
	device list	associate frame identifier with symbolic ID, load DBC file content
	auto threshold setup	assisted threshold configuration
Trigger	source	any input channel or logical channel
	trigger event setup	start of frame, frame type, identifier, identifier + data, symbolic, error condition (any combination of CRC error, bit stuffing error, form error and ACK error)
	identifier setup	frame type (data, remote or both), identifier type (standard or extended); condition =, ≠, ≥, ≤, in range, out of range
	FD bits	FDF and ESI (0, 1, X), BRS (0,1)
	data setup	data pattern up to 8 byte in the complete data range (hex, decimal, octal or binary); condition =, ≠; ≥, ≤, in range, out of range
	symbolic setup	message name, signal name; numeric signal condition =, ≠, ≥, ≤, in range, out of range; enumerated signal condition =, ≠, ≥, ≤
Decode	source	any input channel, math waveform, reference waveform, logical channel
	display type	decoded bus, logical signal, bus + logical signal, tabulated list
	color coding	start of frame, identifier, FD bits, DLC, data payload, CRC, end of frame, error frame, overload frame, CRC error, bit stuffing error
	data format	hex, decimal, octal, binary, ASCII, symbolic
Search	source	any input channel or logical channel
	search event setup	combination of start of frame, frame type, identifier, identifier + data, error condition (any combination of CRC error, bit stuffing error, form error and ACK error) or only symbolic
	event settings	same as trigger event settings

R&S®RTO-K10

SENT triggering and decoding		
Protocol configuration	signal type	data signal
	clock period (clock tick)	1 us to 100 us
	clock tolerance	0 % to 25 %
	data nibbles	1 to 6
	serial message type	none, Short Serial Message and Enhanced Serial Message
	CRC version	Legacy (Feb 2008) and v2010 (Latest)
	CRC calculation	SAE J2716 standard and TLE 4998X
	pause pulse	no, yes, for constant frame length
	frame length in clock ticks (applicable only when pause pulse = constant frame length)	104 to 922
Trigger	source	any analog input channel
	trigger event setup	calibration or sync, transmission sequence, serial message and error condition
	transmission sequence status nibble setup	from 0 to F, condition =, ≠, ≥, ≤, in range, out of range
	transmission sequence data nibbles setup	each nibble value from 0 to F, condition =, ≠, ≥, ≤, in range, out of range
	serial message identifier setup	from 00 to FF, condition =, ≠, ≥, ≤, in range, out of range
	serial message identifier type setup (applicable only when the serial protocol = Enhanced Serial Message in protocol configuration)	4 bit and 8 bit
	serial message data setup	00 to FF (Short Serial Message) 000 to FFF (Enhanced Serial Message with 8 bit ID) 0000 to FFFF (Enhanced Serial Message with 4 bit ID)
	error condition setup	form error, calibration pulse error, pulse period error, CRC error and irregular frame length error
Decode	source	any analog input channel,
	display type	decoded bus, tabulated list
	color coding	transmission sequence: sync/calibration, status, data bits, CRC, pause pulse (optional), calibration pulse error, pulse period error, irregular frame length error and CRC error. serial message: identifier, data, CRC, form error, CRC error
	data format	hex, decimal, octal, binary, ASCII
Search	source	any analog input channel
	search event setup	calibration or sync, transmission sequence, serial message and error condition
	event settings	same as trigger event settings

R&S®RTO-K11

I/Q software interface				
General	function		mixing, filtering, decimation and recording of RF or baseband signals as I/Q samples	
	input signals (2 channel models)		two real RF signals or one complex I/Q signal	
	input signals (4 channel models)		four real RF signals or two complex I/Q signals or two real RF signals and one complex I/Q signal	
	mixer frequency		between 100 Hz and 5 GHz (or mixer deactivated)	
	sampling rate of recorded I/Q samples		between 1 ksample/s and 10 Gsample/s	
	digital filter bandwidth (flat frequency response)		4 % to 80 % of sampling rate	
	sampling rate of recorded I/Q samples		between 1 ksample/s and 10 Gsample/s user-selectable	
	recording length		max. 10 Msample with one or two input signals; max. 6 Msample with three or four input signals; recording length independent of sampling rate	
Trigger	mode		auto or normal	
	operation		triggers on acquired signal after A/D conversion serial bus and MSO trigger not available	
	additional modes		NFC-A, 106 kbps, SENSA_REQ; NFC-B, 106 kbps, SENSB_REQ; NFC-F, 202 kbps or 404 kbps, start of sequence (SoS) length: 48 bit or 96 bit	
Display			magnitude of the downconverted signals	
Amplitude flatness with RF signal input (meas.)	R&S®RTO2002 and R&S®RTO2004	max. used center frequency	with I/Q bandwidth 100 MHz	with I/Q bandwidth 250 MHz
		≤ 100 MHz	±0.10 dB	
		≤ 200 MHz	±0.12 dB	±0.30 dB
		≤ 300 MHz	±0.20 dB	±0.50 dB
		≤ 400 MHz	±0.25 dB	±0.70 dB
		≤ 500 MHz	±0.35 dB	±1.00 dB
	R&S®RTO2012 and R&S®RTO2014	max. used center frequency	with I/Q bandwidth 100 MHz	with I/Q bandwidth 250 MHz
		≤ 100 MHz	±0.10 dB	
		≤ 200 MHz	±0.10 dB	±0.15 dB
		≤ 500 MHz	±0.10 dB	±0.25 dB
		≤ 750 MHz	±0.15 dB	±0.40 dB
		≤ 1 GHz	±0.30 dB	±0.90 dB
	R&S®RTO2022 and R&S®RTO2024	max. used center frequency	with I/Q bandwidth 100 MHz	with I/Q bandwidth 500 MHz
		≤ 100 MHz	±0.10 dB	
		≤ 500 MHz	±0.10 dB	±0.10 dB
		≤ 1 GHz	±0.17 dB	±0.35 dB
		≤ 1.5 GHz	±0.20 dB	±0.50 dB
		≤ 2 GHz	±0.35 dB	±1.00 dB
	R&S®RTO2032 and R&S®RTO2034	max. used center frequency	with I/Q bandwidth 100 MHz	with I/Q bandwidth 500 MHz
		≤ 100 MHz	±0.10 dB	
		≤ 500 MHz	±0.10 dB	±0.10 dB
		≤ 1 GHz	±0.10 dB	±0.35 dB
		≤ 2 GHz	±0.10 dB	±0.35 dB
		≤ 3 GHz	±0.30 dB	±1.30 dB
	R&S®RTO2044	max. used center frequency	with I/Q bandwidth 100 MHz	with I/Q bandwidth 500 MHz
		≤ 100 MHz	±0.10 dB	
		≤ 500 MHz	±0.10 dB	±0.10 dB
		≤ 1 GHz	±0.10 dB	±0.10 dB
		≤ 2 GHz	±0.10 dB	±0.15 dB
		≤ 3 GHz	±0.12 dB	±0.30 dB
		≤ 4 GHz	±0.30 dB	±0.75 dB

R&S®RTO-K12

Basic jitter analysis		
General description	The R&S®RTO-K12 jitter analysis option extends the functionality of the standard R&S®RTO firmware with a suite of measurement, analysis and visualization tools for signal integrity analysis and jitter characterization.	
Waveform measurements	category	jitter
	measurement functions	cycle-to-cycle jitter, N-cycle jitter, cycle-to-cycle width, cycle-to-cycle duty cycle, time-interval error, data rate, unit interval, skew delay, skew phase; the standard time measurements period, frequency and setup/hold are also available in the jitter category for convenience
	track	measurement results displayed as continuous trace that is time-correlated to the measurement source; applicable to time measurements from categories “jitter” and “amplitude and time”; track trace may be used as source for cursor measurements, automatic measurements, math waveforms and reference waveforms
Waveform math	FFT on track	FFT spectrum of the track trace of measurement results
	CDR transform	recovers clock timing from source waveform with software CDR and generates synthetic clock waveform that is time-correlated to source
Software clock data recovery (CDR)	number of CDR instances	up to 2; independently configurable
	algorithm	phase-locked loop (PLL), constant frequency
	configuration	nominal bit rate, PLL order (first or second), PLL loop bandwidth, PLL damping factor, initial phase alignment, result selection during initial synchronization
Jitter wizard	The Jitter wizard assists the user in the step-by-step configuration of the R&S®RTO digital oscilloscope for the measurements period/frequency, cycle-by-cycle jitter, time interval error (TIE) and skew.	
Mask testing with eye mask assistant	primary mask shape	
	type	diamond, square, hexagon, octagon
	dimensions	main and secondary height, main and secondary width, depending on selected shape
	position	vertical offset, horizontal offset
	secondary mask shapes	
	locations	any combination of left, right, top, bottom
	position	horizontal and vertical offset with respect to center of primary mask shape

R&S®RTO-K13

Clock data recovery (CDR)		
General description	The R&S®RTO-K13 realtime clock data recovery option activates the hardware CDR circuitry integrated into the R&S®RTO digital oscilloscope. It provides realtime clock recovery for non-return-to-zero (NRZ) serial data up to 5.0 Gbps. The recovered clock may be used for triggering and jitter analysis.	
Hardware clock data recovery (CDR)	description	fully digital implementation of PLL-based clock data recovery
	sources	
	R&S®RTO2002, R&S®RTO2012, R&S®RTO2022, R&S®RTO2032	channel 1, channel 2
	R&S®RTO2004, R&S®RTO2014, R&S®RTO2024, R&S®RTO2034, R&S®RTO2044	channel 1, channel 2, channel 3, channel 4
	configuration parameters	PLL order (first or second), nominal bit rate, loop bandwidth, relative bandwidth, damping factor, unit interval offset
	bit rate range	
	R&S®RTO2002, R&S®RTO2004, R&S®RTO2012, R&S®RTO2014, R&S®RTO2022, R&S®RTO2024, R&S®RTO2032, R&S®RTO2034	200 kbps to 2.5 Gbps
	R&S®RTO2044	200 kbps to 2.5 Gpbs standard, 400 kbps to 5.0 Gbps when operating at 20 Gsample/s realtime sampling rate [5]
	relative bandwidth	1/500 to 1/3000 of the nominal bit rate
	damping factor	0.5 to 1.0; relevant for second order PLL only
	unit interval offset	0.0 to 1.0
Trigger modes	CDR	triggers on clock signal recovered from the trigger source signal; phase of the trigger instant user-selectable as fraction of bit period
	serial pattern	main trigger mode “serial pattern” supports the hardware CDR as additional clock source; sampling point user-selectable as fraction of bit period
Jitter analysis	The data and clock timing information of the hardware CDR may be acquired in realtime concurrently to the input data waveform. Analysis of the realtime CDR timing information is possible by means of compatible measurement, analysis and visualization tools provided in the R&S®RTO-K12 jitter analysis option.	
	measurement functions	time-interval error (TIE), data rate, unit interval
	math functions	CDR transform interprets the acquired clock timing information and generates a synthetic clock waveform that is time-correlated to the input data waveform

[5] The front-end of the R&S®RTO2044 samples at 20 Gsample/s when: at most one channel from each pair {channel1, channel2} and {channel3, channel4} is active; and the user-selected sampling resolution in realtime sampling mode or interpolated time sampling mode is 50 ps or smaller.

R&S®RTO-K17

High definition mode		
General description	The R&S®RTO-K17 high definition mode increases the numeric resolution of the waveform signal by using digital filtering, leading to a reduced noise. Because of the R&S®RTO digital trigger concept the signals with increased numeric resolution are used as input for triggering.	
Numeric resolution	bandwidth	bit resolution
	10 kHz to 50 MHz	16 bit
	100 MHz	14 bit
	200 MHz	13 bit
	300 MHz	12 bit
	500 MHz	12 bit
	1 GHz	10 bit
Realtime sampling rate		max. 5 Gsample/s on each channel
Input sensitivity		Input sensitivity range is extended down to 500 µV/div; 500 µV/div is a magnification of the 1 mV/div setting.

R&S®RTO-K19

Zone trigger		
General description	The R&S®RTO-K19 zone trigger enables the triggering on user-defined zones drawn on the display.	
Source		acquired waveforms (input channels), math waveforms
Supported acquisition modes	decimation modes	sample, peak detect, high resolution, root mean square
	high definition mode	with R&S®RTO-K17 option
Zone definition	number of zones	up to 8
	shapes	rectangles, polygones
	types	must intersect, must not intersect
	combination of zones	logical combination of zones of multiple sources using Boolean expressions
Trigger compatibility		compatible with the trigger modes edge, glitch, width, runt, window, timeout, interval, slew rate, data2clock, pattern, state, serial pattern, trigger qualification, and sequence trigger

R&S®RTO-K21

The R&S®RTO-K21 option is available for R&S®RTO2004 (high speed not supported), R&S®RTO2014, R&S®RTO2024, R&S®RTO2034 and R&S®RTO2044 models only. The option is used in combination with the free-of-charge R&S®ScopeSuite PC software, which can be downloaded from the Rohde & Schwarz website. R&S®RTO-K21 makes it possible to perform USB 2.0 compliance test measurements with R&S®ScopeSuite, including tests for USB 2.0 (high speed), USB 1.1 (full speed) and USB 1.0 (low speed) with the R&S®RTO. R&S®ScopeSuite supports the R&S®RT-ZF1 USB 2.0 compliance test fixture set and the Allion USB test fixture solutions and the USB-IF signal quality board device/host; it requires Windows 7.

Supported USB 2.0 compliance tests		
USB device test	high speed	signal quality (EL_2, 4, 5, 6, 7); packet parameters (EL_21, 22, 25); chirp timing (EL_28, 29, 31); suspend/resume/reset timing (EL_27, 28, 38, 39, 40); test J/K, SE0_NAK (EL_8, 9); receiver sensitivity (EL_16, 17, 18)
	full speed and low speed	full speed signal quality; back voltage; inrush current
USB host test	high speed	signal quality (EL_2, 3, 6, 7); packet parameters (EL_21, 22, 23, 25, 55); chirp timing (EL_33, 34, 35); suspend/resume/reset timing (EL_39, 41); test J/K, SE0_NAK (EL_8, 9)
	full speed and low speed	low speed signal quality downstream; full speed signal quality downstream; drop; droop
USB hub test	high speed	signal quality upstream (EL_2, 4, 6, 7); signal quality downstream (EL_2, 3, 6, 7); jitter downstream (EL_47); packet parameters upstream (EL_21, 22, 25); hub receiver sensitivity upstream (EL_16, 17, 18); repeater downstream (EL_42, 43, 44, 45, 48); repeater upstream (EL_42, 43, 44, 45); chirp timing upstream (EL_28, 29, 31); suspend/resume/reset timing upstream (EL_27, 28, 38, 39, 40); test J/K, SE0_NAK upstream (EL_8, 9); test J/K, SE0_NAK downstream (EL_8, 9)
	full speed and low speed	low speed signal quality downstream; full speed signal quality upstream; full speed signal quality downstream; inrush current upstream; drop downstream; droop downstream; back voltage

R&S®RTO-K22

The option is used in combination with the free-of-charge R&S®ScopeSuite PC software, which can be downloaded from the Rohde & Schwarz website. R&S®RTO-K22 makes it possible to perform Ethernet compliance test measurements with R&S®ScopeSuite, including tests for 10BASE-T, 100BASE-TX and 1000BASE-T with the R&S®RTO. R&S®ScopeSuite supports the R&S®RT-ZF2 Ethernet compliance test fixture set; it requires Windows 7. The chapters after the test cases refer to IEEE 802.3-2012.

Supported Ethernet 10G compliance tests		
1000BASE-T	with/without disturber	with/without TX_CLK transmitter distortion (40.6.1.2.4)
		peak differential output voltage (40.6.1.2.1)
		maximum output droop (40.6.1.2.2)
		differential output templates (40.6.1.2.3)
	with TX_CLK	jitter master mode (40.6.1.2.5), jitter slave mode (40.6.1.2.5)
	without TX_CLK	jitter master mode (40.6.1.2.5)
	common	MDI return loss (40.8.3.1), common-mode output voltage (40.8.3.3)
100BASE-TX		amplitude domain tests (9.1.2.2, 9.1.3 and 9.1.4)
		rise and fall times (9.1.6)
		peak to peak duty cycle distortion (9.1.8)
		peak to peak transmitter jitter (9.1.9)
		active output interface template (annex J)
		transmitter return loss (9.1.5)
		receiver return loss (9.2.2)
10BASE-T	no TPM	link test pulse template (14.3.1.2.1)
		TP_IDL template (14.3.1.2.1)
		peak differential voltage (14.3.1.2.1)
		harmonic content (14.3.1.2.1)
		output timing jitter (14.3.1.2.3)
	with TPM	link test pulse template (14.3.1.2.1)
		TP_IDL template (14.3.1.2.1)
		MAU template (14.3.1.2.1)
		output timing jitter (14.3.1.2.3)
	common	transmitter return loss (14.3.1.2.2), receiver return loss (14.3.1.3.4)
		common-mode output voltage (14.3.1.2.5)

R&S®RTO-K23

The R&S®RTO-K23 option is available for R&S®RTO2022, R&S®RTO2024, R&S®RTO2032, R&S®RTO2034 and R&S®RTO2044 models only. The option is used in combination with the free-of-charge R&S®ScopeSuite PC software, which can be downloaded from the Rohde & Schwarz website. R&S®RTO-K23 makes it possible to perform Ethernet compliance test measurements with R&S®ScopeSuite, including tests for 10GBASE-T with the R&S®RTO. R&S®ScopeSuite supports the R&S®RT-ZF2 Ethernet compliance test fixture set; it requires Windows 7. The chapters after the test cases refer to IEEE 802.3-2012.

Supported Ethernet compliance tests		
10GBASE-T		maximum output droop (55.5.3.1)
		transmitter linearity (55.5.3.2)
		transmitter timing jitter master mode (55.5.3.3)
		transmitter timing jitter slave mode (55.5.3.3)
		transmitter power spectral density (55.5.3.4) [6]
		transmitter power level (55.5.3.4) [5]
		transmitter clock frequency (55.5.3.5)
		MDI return loss (55.8.2.1)

R&S®RTO-K24

The option is used in combination with the free-of-charge R&S®ScopeSuite PC software, which can be downloaded from the Rohde & Schwarz website. R&S®RTO-K24 makes it possible to perform BroadR-Reach® compliance test measurements with R&S®ScopeSuite. R&S®ScopeSuite supports the R&S®RT-ZF2 Ethernet compliance test fixture set; it requires Windows 7. The chapters after the test cases refer to OPEN Alliance BroadR-Reach® (OABR) V3.2 and V3.0 and IEEE P802.3bw as subchapters of chapter 96.

Supported BroadR-Reach® compliance tests		
BroadR-Reach®		transmitter output droop (5.4.1)
		transmitter distortion with and without disturber (5.4.2)
		transmitter timing jitter master mode (5.4.3)
		transmitter timing jitter slave mode (5.4.3)
		transmitter power spectral density (5.4.4)
		transmitter clock frequency (5.4.5)
		MDI return loss (8.2.2)

[6] Requires an oscilloscope model with a bandwidth higher than or equal 3 GHz.

R&S®RTO-K26

The R&S®RTO-K26 option is available for R&S®RTO 2024, R&S®RTO2034 nad R&S®RTO2044 models only. The option is used in combination with the free-of-charge R&S®ScopeSuite PC software, which can be downloaded from the Rohde & Schwarz website. R&S®RTO-K26 makes it possible to perform D-PHY compliance test measurements with R&S®ScopeSuite. R&S®ScopeSuite requires Windows 7. The numbers behind the test refer to the MIPI CTS for D-PHY V1.1 .

Supported D-PHY compliance tests		
DPHY	group 1 (7 tests): data lane LP-TX signaling requirements	data lane LP-TX Thevenin output high level voltage (V_{OH}) – 1.1.1
		data lane LP-TX Thevenin output low level voltage (V_{OL}) – 1.1.2
		data lane LP-TX from 15 % to 85 % rise time (T_{RLP}) – 1.1.3
		data lane LP-TX from 85 % to 15 % fall time (T_{FLP}) – 1.1.4
		data lane LP-TX slew rate versus C_{LOAD} ($\delta V/\delta t_{SR}$) – 1.1.5
		data lane LP-TX pulse width of exclusive-OR clock ($T_{LP\text{-}PULSE\text{-}TX}$) – 1.1.6
		data lane LP-TX period of exclusive-OR clock ($T_{LP\text{-}PER\text{-}TX}$) – 1.1.7
	group 2 (5 tests): clock lane LP-TX signaling requirements	clock lane LP-TX Thevenin output high level voltage (V_{OH}) – 1.2.1
		clock lane LP-TX Thevenin output low level voltage (V_{OL}) – 1.2.2
		clock lane LP-TX from 15 % to 85 % rise time (T_{RLP}) – 1.2.3
		clock lane LP-TX from 85 % to 15 % fall time (T_{FLP}) – 1.2.4
		clock lane LP-TX slew rate versus C_{LOAD} ($\delta V/\delta t_{SR}$) – 1.2.5
	group 3 (16 tests): data lane HS-TX signaling requirements	data lane HS entry: data lane T_{LPX} value – 1.3.1
		data lane HS entry: data lane $T_{HS\text{-}PREPARE}$ value – 1.3.2
		data lane HS entry: data lane $T_{HS\text{-}PREPARE}$ + $T_{HS\text{-}ZERO}$ value – 1.3.3
		data lane HS-TX differential voltages $V_{OD(0)}$ and $V_{OD(1)}$ – 1.3.4
		data lane HS-TX differential voltage mismatch ΔV_{OD} – 1.3.5
		data lane HS-TX single-ended output voltages $V_{OHHS(DP)}$ and $V_{OHHS(DN)}$ – 1.3.6
		data lane HS-TX static common-mode voltages $V_{CMTX(1)}$ and $V_{CMTX(0)}$ – 1.3.7
		data lane HS-TX static common-mode voltage mismatch $\Delta V_{CMTX(1.0)}$ – 1.3.8
		data lane HS-TX dynamic common-level variations from 50 MHz to 450 MHz $\Delta V_{CMTX(LF)}$ – 1.3.9
		data lane HS-TX dynamic common-level variations above 450 MHz $\Delta V_{CMTX(HF)}$ – 1.3.10
		data lane HS-TX from 20 % to 80 % rise time t_R – 1.3.11
		data lane HS-TX from 80 % to 20 % fall time t_F – 1.3.12
		data lane HS exit: $T_{HS\text{-}TRAIL}$ value – 1.3.13
		data lane HS exit: from 30 % to 85 % post-EoT rise time T_{REOT} – 1.3.14
		data lane HS exit: T_{EOT} value – 1.3.15
		data lane HS exit: $T_{HS\text{-}EXIT}$ value – 1.3.16

DPHY	group 4 (18 tests): clock lane HS-TX signaling requirements	clock lane HS entry: T_{LPX} value – 1.4.1
		clock lane HS entry: $T_{CLK-PREPARE}$ value – 1.4.2
		clock lane HS entry: $T_{CLK-PREPARE}$ + $T_{CLK-ZERO}$ value – 1.4.3
		clock lane HS-TX differential voltages $V_{OD(0)}$ and $V_{OD(1)}$ – 1.4.4
		clock lane HS-TX differential voltage mismatch ΔV_{OD} – 1.4.5
		clock lane HS-TX single-ended output voltages $V_{OHHS(DP)}$ and $V_{OHHS(DN)}$ – 1.4.6
		clock lane HS-TX static common-mode voltages $V_{CMTX(1)}$ and $V_{CMTX(0)}$ – 1.4.7
		clock lane HS-TX static common-mode voltage mismatch $\Delta V_{CMTX(1,0)}$ – 1.4.8
		clock lane HS-TX dynamic common-level variations from 50 MHz to 450 MHz $\Delta V_{CMTX(LF)}$ – 1.4.9
		clock lane HS-TX dynamic common-level variations above 450 MHz $\Delta V_{CMTX(HF)}$ – 1.4.10
		clock lane HS-TX from 20 % to 80 % rise time t_R – 1.4.11
		clock lane HS-TX from 80 % to 20 % fall time t_F – 1.4.12
		clock lane HS exit: $T_{CLK-TRAIL}$ value – 1.4.13
		clock lane HS exit: from 30 % to 85 % post-EoT rise time T_{REOT} – 1.4.14
		clock lane HS exit: T_{EOT} value – 1.4.15
		clock lane HS exit: $T_{HS-EXIT}$ value – 1.4.16
		clock lane HS clock instantaneous: UI_{INST} value – 1.4.17
		clock lane HS clock delta UI: (ΔUI) value – 1.4.18
	group 5 (4 tests): HS-TX clock-to-data lane timing requirements	HS entry: $T_{CLK-PRE}$ value – 1.5.1
		HS exit: $T_{CLK-POST}$ value – 1.5.2
		HS clock rising edge alignment to first payload bit – 1.5.3
		data-to-clock skew ($T_{SKEW[TX]}$) – 1.5.4

R&S®RTO-K31

Power analysis		
General description	The R&S®RTO-K31 power analysis option extends the R&S®RTO firmware with measurement functionality focused on switched mode power supplies (SMPS) and DC/DC converters.	
Input	quality	evaluation of power quality at an AC input; measures real power, apparent power, reactive power, power factor and phase angle of power, frequency, crest factor, RMS of voltage and current
	harmonics	measures up to the 40th harmonic of the incoming line frequency; precompliance checking for IEC 61000-3-2 (A, B, C, D), RTCA DO-160, MIL-STD-1399, max. limit checks
	inrush current	measures peak inrush current; multiple measurement zones configurable with analysis of the post-inrush behavior
Switching/control loop	slew rate	The slope of current or voltage is measured at start and end of the switching cycle.
	modulation	measures modulation of switching frequency and duty cycle under steady state and start-up conditions
	dynamic on-resistance	measures resistance of the switching transistor(s) in active state
Power path	efficiency (only for 4 channel devices)	measures input and output power to calculate the efficiency of an SMPS
	loss	measures switching loss and conduction loss of a power device
	safe operating area (SOA)	checks violation of voltage and current limits in which a power device can operate without damage; current versus voltage view (linear or log); violation mask is user-defined and editable in linear and log-log views
	turn on/off	measures relationship between AC and DC current, when turning the SMPS off and on
Output	ripple	measures AC components of output voltage and current, AC RMS, frequency, duty cycles, min./max./peak-to-peak amplitude
	spectrum	FFT analysis of output, measurement of frequency peaks
	transient response	This measurement captures the device behavior between the event of load changes and stabilization. includes peak (voltage, time), settling time, rise time, overshoot and delay
Deskew	automated	By using the R&S®RT-ZF20 probe deskew and calibration test fixture and Rohde & Schwarz voltage and current probes, the skew between the voltage and current signal is compensated automatically.
Reporting	easy reporting: Click to save a measurement. Report generation using user-selected test results from historical and currently-active tests. Put repeated and/or different measurements in one report.	

R&S®RTO-K40

MIPI RFFE triggering and decoding		
Protocol configuration	signal type	two channel, single-ended
	bit rate	auto-detected, up to 26 Mbps
	auto threshold setup	assisted threshold configuration
	source (SCLK, SDATA)	any two input channels, math waveforms, reference waveforms, or logical channels
Trigger	trigger event setup	sequence start, sequence stop, register 0 write, register write, register read, extended register write, extended register read, extended register write long, extended register read long, error condition types
	sequence start setup	4 bit slave address; conditions =, ≠, <, ≤, >, ≥, in range, out of range
	sequence stop setup	4 bit slave address; conditions =, ≠, <, ≤, >, ≥, in range, out of range
	register 0 write setup	4 bit slave address, 7 bit data word; conditions =, ≠, <, ≤, >, ≥, in range, out of range for each of these options
	register write/read	4 bit slave address, 5 bit register address, 8 bit data word; conditions =, ≠, <, ≤, >, ≥, in range, out of range for each of these options
	extended register write/read	4 bit slave address; 8 bit address, byte count : 0 to 15 (inclusive), data pattern: 1 to 16 bytes (hex or binary); conditions =, ≠, <, ≤, >, ≥, in range, out of range for each of these options; index: 1 to 16 selects the specific data frame byte; conditions =, ≠, <, ≤, >, ≥, in range
	extended register write long/read long	4 bit slave address, 8 bit address, byte count : 0 to 7 (inclusive), data pattern: 0 to 8 bytes (hex or binary); conditions =, ≠, <, ≤, >, ≥, in range, out of range for each of these options; index: 1 to 8 selects the specific data frame byte; conditions =, ≠, <, ≤, >, ≥, in range
	error condition	SSC error; length error, bus park error, parity error, no response, unknown sequence, minimum gap between frames: 2 ns to 100 ns maximum gap between frames: 2 ns to 1 ms
Decode	display type	decoded bus, logical signal, bus + logical signal, tabulated list
	color coding	sequence, frame, error
	data format	hex, octal, binary, ASCII, signed, unsigned
Search	search event setup	sequence start, sequence stop, register 0 write, register write, register read, extended register write, extended register read, extended register write long, extended register read long, error condition types
	event settings	same as trigger event settings

R&S®RTO-K42

MIPI D-PHY triggering and decoding		
Protocol configuration	signal type	clock, data (differential or single ended)
	data rate	selectable without clock lane (1Mbps to 2.5 Gbps) auto detect with clock lane
	source	any input channels, math waveforms, reference waveforms
	variants	D-PHY v. 1.2, CSI-2 v.1.2, DSI v. 1.3
Trigger	trigger event setup	HS start of packet HS end of packet HS packet header HS data LP escape mode LP lane turnaround LP HS request
	HS packet header setup	virtual channel, data type, word count; conditions =, ≠, <, ≤, >, ≥, in range, out of range for data and word count
	HS data	virtual channel, data type, word count, data value, data index; conditions =, ≠, <, ≤, >, ≥, in range, out of range for data count, word count, data value
	LP escape mode	escape mode, data value, data index; conditions =, ≠, <, ≤, >, ≥, in range, out of range for escape mode and data value
Decode	display type	decoded bus, tabulated list, details, decode layers
	color coding	high speed: frames according to trace, cells low power: escape word, data word
	data format	hex, octal, binary, ascii, signed, unsigned
Search	search event setup	HS start of packet HS end of packet HS packet header HS data LP escape mode LP lane turnaround LP HS request
	event settings	same as trigger event setup

R&S®RTO-K50

Manchester and NRZ serial triggering and decoding		
Protocol configuration	signal type	selectable, one channel, differential or single-ended, two channel, differential or single-ended
	bit rate	auto detected, adjustable
	auto threshold setup	assisted threshold configuration
	source	analog, math. channels
	bit encoding variants	Manchester, Manchester II, NRZ clocked, NRZ unclocked
	properties	active state (high/low), idle state (high/low), clock edge (first/second)
Frame format	frame	multiple frame management, frame identification and sync, variable length frames, variable number of cells
	cells	name, size (bits), numeric format, bit order, color
	file storage of frame format	save/load as xml files
Trigger	variants	all supported bit encodings
	trigger event setup	frame start, pattern
	frame start	gap, start bit
	pattern	up to 256 bit pattern within 65 535 bit frame [8]
Decode	display type	decoded bus, logical signal, bus signal, tabulated list, result details
	color coding	according to cell configuration table
	data format	according to cell configuration table

R&S®RTO-K52

8b10b decoding		
Protocol configuration	signal type	one/two channel, differential, single-ended
	bit rate	selectable/adjustable auto configuration, ideal for bitrate up to 6.25 Gbit/s
	auto threshold setup	assisted threshold configuration
	one click setup	convenient way for perfect decode results; auto scaling of waveforms, auto threshold and bitrate estimation on one click
	source (differential, single-ended D+/D–)	full combination of either analog, math, reference channels
	variants	all layer 1 (physical layer) encoded 8b/10b protocols, recommended for Ethernet, FibreChannel 1G, 2G, PCI Express®, Serial ATA, Serial Rapid IO (SRIO), XAUI
Decode	display type	decoded bus, bus signal, tabulated list, details
	color coding	sync symbol, K symbols, data (Dx.y) coding and error coding
	data format	hex, 10bit and K/D representation
Search	search event setup	complex combination of symbols, errors
	symbol combinations	K/D symbols scenario, selectable search format (8bit, 10bit and K/D symbols)
	error	disparity, glitching and unknown error

[8] The pattern trigger will not be effective after Manchester violations.

R&S®RTO-K55

MDIO triggering and decoding		
Protocol configuration	bit rate	up to 5 Mbps (auto-detected)
	auto threshold setup	assisted threshold configuration for MDIO triggering and decoding
	device list	associate frame address with symbolic ID
Trigger	source (clock and data)	any input channel or logical channel
	trigger event setup	start, stop, ST, OP, PHY address, register address, data
	ST setup	01 (clause 22), 00 clause 45, any
	OP setup	address, write, post read, read, any
	PHY address setup	5 bit address (hex, decimal, octal or binary); equal
	PHY register (clause 22)/device type (clause 45) setup	5 bit value (hex, decimal, octal or binary); equal
	data (clause 22)/data/address (clause 45)	16 bit value (hex, decimal, octal or binary); equal
Decode	source (clock and data)	any input channel, math waveform, reference waveform, logical channel
	display type	decoded bus, logical signal, bus + logical signal, tabulated list
	color coding	frame, PHY address, PHY register, address, data, turnaround
	PHYAD/PRTAD	symbolic names for user defined addresses
	address/data field format	hex, decimal, octal, binary, ASCII
Search	source (clock and data)	any input channel, math waveform, reference waveform, logical channel
	search event setup	start, stop, ST, OP, PHY address, register address, data
	event settings	same as trigger event settings

R&S®RTO-K60

USB 1.0/1.1/2.0/HSIC triggering and decoding		
Protocol configuration	signal type	single-ended, differential
	protocol type	low, full, high speed and HSIC
	bit rate	standard bit rates (1.5/12/480 Mbit/s)
	source	any input channel
	probe type	
	for low and full speed	single-ended probe
	for high speed	differential probe (R&S®ZDx)
	for HSIC	single-ended probe(R&S®ZSx)
	auto threshold setup	assisted threshold configuration for USB triggering and decoding
Trigger	trigger event setup	start of packet, end of packet, PID token (IN, OUT, SETUP, SOF), PID data (Data0, Data1, Data2 [9], MData [9]), PID handshake (ACK, NAK, STALL, NYET [9]), PID special (PRE [10], ERR [9], SPLIT [9], PING [9]); bus state (reset [10], resume [10], suspend [10]); error condition
	address, endpoint and frame setup SC, port, SEU, ET check (SPLIT) [5]	condition =, ≠, ≥, ≤, in range, out of range
	data setup	data pattern up to 4 byte (hex, decimal, octal, binary or ASCII), bit separately configurable (1, 0 or don't care); condition =, ≠; position based or window based triggering (first occurrence in packet payload)
	error condition	any error, PID error, CRC5 error, CRC16 error, bit stuffing error, unexpected PID, SE1 error [10] and glitching error
Decode	source	any input channel, math waveform
	display type	decoded bus, logical signal, bus + logical signal, tabulated list
	color coding	packet identifier, payload length, frame, address, endpoint , data payload, CRC5, CRC16, error condition
	data format	hexadecimal, decimal, octal, binary, ASCII, unsigned
Search	search event setup	combination of start of packet, PID token (IN, OUT, SETUP, SOF), PID data (Data0, Data1, Data2 [9], MData [9]), PID handshake (ACK, NAK, STALL, NYET [9]), PID special (PRE [10], ERR [9], SPLIT [9], PING [9]); error condition (any error, PID error, CRC5 error, CRC16 error, bit stuffing error, unexpected PID, SE1 error [10] and glitching error)
	address, endpoint and frame setup SC, port, SEU, ET check (SPLIT)	condition =, ≠, ≥, ≤, in range, out of range
	data setup	data pattern up to 4 byte (hex, decimal, octal, binary or ASCII), bit separately configurable (1, 0 or don't care); condition =, ≠; position based or window based triggering (first occurrence in packet payload)
	error condition	any error, PID error, CRC5 error, CRC16 error, bit stuffing error, unexpected PID, SE1 error [10] and glitching error

[9] Only available in high speed and HSIC.

[10] Only available in low and full speed.

R&S®RTO-K65

SpaceWire serial triggering and decoding		
Protocol configuration	signal type	two channels: strobe and data (differential or single-ended)
	bit rate	auto adjust (strobe + data)
	source	any analog input channels, logical channels [11], math channels, reference channels
	polarity	normal, inverted
Trigger	trigger event setup	control frame, data pattern, null frame, time code, error condition
	control frame setup	any, FCT, EOP, EEP
	data pattern setup	8 bit (condition =, ≠, <, >, ≥, ≤, in range, out of range)
	time code setup	8 bit (condition =, ≠, <, >, ≥, ≤, in range, out of range)
	errors condition setup	parity, ESC
Decode	display type	decoded bus, logical signal, bus + logical signal, tabulated list, decode layers
	color coding	control frame, data frame, null frame, time code
	data format	hex
Search	search event setup	control frame, data pattern, null frame, time code, error
	event settings	same as trigger event settings

[11] SpaceWire protocol trigger on logical channels is not available.

R&S®RTO-K92

The R&S®RTO-K92 option is available for R&S®RTO2004, R&S®RTO2014, R&S®RTO2024, R&S®RTO2034 and R&S®RTO2044 models only. The option is used in combination with the free-of-charge R&S®ScopeSuite PC software, which can be downloaded from the Rohde & Schwarz website. R&S®RTO-K92 makes it possible to perform eMMC (HS200, HS400) compliance test measurements with R&S®ScopeSuite. R&S®ScopeSuite requires Windows 7.

Supported eMMC compliance tests		
HS200 (JESD84-B50)	CLK (10.5.2, 10.8.1)	bus signal levels tests (VIH, VIL)
		interface timing tests (t_{Period}, rise time, fall time, duty cycle)
	CMD push pull (10.5.2, 10.8.1)	bus signal levels tests (VIH, VIL, VOH, VOL)
		interface timing tests (setup time, hold time)
	CMD open drain (10.5.1)	bus signal levels tests (VOH, VOL)
	DAT data write (10.5.2, 10.8.1)	bus signal levels tests (VIH, VIL)
		interface timing tests (setup time, hold time)
	DAT data read (10.5.2, 10.8.1)	bus signal levels tests (VOH, VOL)
HS400 (JESD84-B50)	CLK (10.5.2, 10.10.1)	bus signal levels tests (VIH, VIL)
		interface timing tests (t_{Period}, slew rate, duty cycle distortion, minimum pulse width)
	CMD push pull (10.5.2, 10.10.1)	bus signal levels tests (VIH, VIL, VOH, VOL)
		interface timing tests (setup time, hold time)
	CMD open drain (10.5.1)	bus signal levels tests (VOH, VOL)
	DAT data write (10.5.2, 10.10.1)	bus signal levels tests (VIH, VIL)
		interface timing tests (setup time, hold time, slew rate)
	DAT data read (10.5.2, 10.10.2)	bus signal levels tests (VOH, VOL)
		interface timing tests (output skew, output hold skew, slew rate)
	data strobe for data read (10.5.2, 10.10.1)	bus signal levels tests (VOH, VOL)
		interface timing tests (t_{Period}, slew rate, duty cycle distortion, minimum pulse width)

Ordering information

Designation	Type	Order No.
Base unit (including standard accessories: 500 MHz passive probe (10:1) per channel, accessories bag, quick start guide, CD with manual, power cord)		
Digital Oscilloscope		
600 MHz, 10 Gsample/s, 50/100 Msample, 2 channels	R&S®RTO2002	1329.7002.02
600 MHz, 10 Gsample/s, 50/200 Msample, 4 channels	R&S®RTO2004	1329.7002.04
1 GHz, 10 Gsample/s, 50/100 Msample, 2 channels	R&S®RTO2012	1329.7002.12
1 GHz, 10 Gsample/s, 50/200 Msample, 4 channels	R&S®RTO2014	1329.7002.14
2 GHz, 10 Gsample/s, 50/100 Msample, 2 channels	R&S®RTO2022	1329.7002.22
2 GHz, 10 Gsample/s, 50/200 Msample, 4 channels	R&S®RTO2024	1329.7002.24
3 GHz, 10 Gsample/s, 50/100 Msample, 2 channels	R&S®RTO2032	1329.7002.34
3 GHz, 10 Gsample/s, 50/200 Msample, 4 channels	R&S®RTO2034	1329.7002.34
4 GHz, 20 Gsample/s, 50/200 Msample, 4 channels	R&S®RTO2044	1329.7002.44
Hardware options (plug-in)		
Mixed Signal Option, 400 MHz	R&S®RTO-B1	1304.9901.03
OCXO 10 MHz	R&S®RTO-B4	1304.8305.02
GPIB Interface	R&S®RTO-B10	1304.8311.03
Additional Solid State Disk	R&S®RTO-B19	1329.7048.02
Memory Upgrade, 100 Msample per channel	R&S®RTO-B101	1329.7060.02
Memory Upgrade, 200 Msample per channel	R&S®RTO-B102	1329.7077.02
Memory Upgrade, 400 Msample per channel	R&S®RTO-B104	1329.7083.02
Memory Upgrade, 1 Gsample per channel, for R&S®RTO2002/12/22/32	R&S®RTO-B110	1329.7090.02
Memory Upgrade, 1 Gsample per channel, for R&S®RTO2004/14/24/34/44	R&S®RTO-B110	1329.7090.04
Bandwidth upgrades [12]		
Upgrade of R&S®RTO2002/4 to 1 GHz bandwidth	R&S®RTO-B201	1329.7102.02
Upgrade of R&S®RTO2002/4 to 2 GHz bandwidth	R&S®RTO-B202	1329.7119.02
Upgrade of R&S®RTO2002/4 to 3 GHz bandwidth	R&S®RTO-B203	1329.7125.02
Upgrade of R&S®RTO2004 to 4 GHz bandwidth	R&S®RTO-B204	1329.7131.02
Upgrade of R&S®RTO2012/4 to 2 GHz bandwidth	R&S®RTO-B212	1329.7154.02
Upgrade of R&S®RTO2012/4 to 3 GHz bandwidth	R&S®RTO-B213	1329.7160.02
Upgrade of R&S®RTO2014 to 4 GHz bandwidth	R&S®RTO-B214	1329.7177.02
Upgrade of R&S®RTO2022/4 to 3 GHz bandwidth	R&S®RTO-B223	1329.7190.02
Upgrade of R&S®RTO2022/4 to 4 GHz bandwidth	R&S®RTO-B224	1329.7202.02
Upgrade of R&S®RTO2034 to 4 GHz bandwidth	R&S®RTO-B234	1329.7225.02
Software options		
Serial triggering and decoding		
I²C/SPI Serial Decoding	R&S®RTO-K1	1329.7260.02
UART/RS-232/RS-422/RS-485 Serial Decoding	R&S®RTO-K2	1329.7277.02
CAN/LIN Serial Triggering and Decoding	R&S®RTO-K3	1329.7283.02
FlexRay™ Serial Triggering and Decoding	R&S®RTO-K4	1329.7290.02
I²S Serial Triggering and Decoding	R&S®RTO-K5	1329.7302.02
MIL-STD-1553 Serial Triggering and Decoding	R&S®RTO-K6	1329.7319.02
ARINC 429 Serial Triggering and Decoding	R&S®RTO-K7	1329.7325.02
Ethernet Serial Decoding	R&S®RTO-K8	1329.7331.02
CAN-FD Serial Triggering and Decoding	R&S®RTO-K9	1329.7348.02
SENT Serial Triggering and Decoding	R&S®RTO-K10	1329.7354.02
MIPI RFFE Serial Triggering and Decoding	R&S®RTO-K40	1329.7519.02
MIPI D-PHY Serial Triggering and Decoding	R&S®RTO-K42	1329.7525.02
Manchester and NRZ Serial Triggering and Decoding	R&S®RTO-K50	1329.7531.02
8b10b Serial Decoding	R&S®RTO-K52	1329.7548.02
MDIO Serial Triggering and Decoding	R&S®RTO-K55	1329.7554.02
USB 1.0/1.1/2.0/HSIC Serial Triggering and Decoding	R&S®RTO-K60	1329.7560.02
SpaceWire Serial Triggering and Decoding	R&S®RTO-K65	1326.2868.02
Compliance tests		
USB 2.0 Compliance Test	R&S®RTO-K21	1329.7454.02
Ethernet Compliance Test	R&S®RTO-K22	1329.7460.02
Ethernet 10G Compliance Test	R&S®RTO-K23	1329.7477.02
BroadR-Reach® Compliance Test	R&S®RTO-K24	1329.7483.02
D-PHY Compliance Test	R&S®RTO-K26	1329.7490.02
eMMC Compliance Test	R&S®RTO-K92	1333.0444.02

[12] The bandwidth upgrade is performed at a Rohde & Schwarz service center, where the oscilloscope will also be calibrated.

Designation	Type	Order No.
Analysis		
I/Q Software Interface	R&S®RTO-K11	1329.7360.02
Jitter Analysis	R&S®RTO-K12	1329.7377.02
Clock Data Recovery	R&S®RTO-K13	1329.7383.02
High Definition Mode	R&S®RTO-K17	1329.7419.02
ZoneTrigger	R&S®RTO-K19	1329.7431.02
Power Analysis	R&S®RTO-K31	1329.7502.02
Probes		
500 MHz, passive, 10:1, 1 MΩ, 9.5 pF, max. 400 V	R&S®RT-ZP10	1409.7550.00
400 MHz, passive, high-voltage, 100:1, 50 MΩ, 7.5 pF, 1 kV (RMS)	R&S®RT-ZH10	1409.7720.02
400 MHz, passive, high-voltage, 1000:1, 50 MΩ, 7.5 pF, 1 kV (RMS)	R&S®RT-ZH11	1409.7737.02
8.0 GHz, passive, transmission line, 10:1, 500 Ω, 0.3 pF, 20 V (RMS)	R&S®RT-ZZ80	1409.7608.02
1.0 GHz, active, 1 MΩ || 0.8 pF	R&S®RT-ZS10E	1418.7007.02
1.0 GHz, active, 1 MΩ || 0.8 pF, R&S®ProbeMeter, micro button	R&S®RT-ZS10	1410.4080.02
1.5 GHz, active, 1 MΩ || 0.8 pF, R&S®ProbeMeter, micro button	R&S®RT-ZS20	1410.3502.02
3.0 GHz, active, 1 MΩ || 0.8 pF, R&S®ProbeMeter, micro button	R&S®RT-ZS30	1410.4309.02
6.0 GHz, active, 1 MΩ || 0.3 pF, R&S®ProbeMeter, micro button	R&S®RT-ZS60	1418.7307.02
100 MHz, high-voltage, active, differential, 8 MΩ || 3.5 pF, 1 kV (RMS) (CAT III)	R&S®RT-ZD01	1422.0703.02
1.5 GHz, active, differential, 1 MΩ || 0.6 pF, R&S®ProbeMeter, micro button	R&S®RT-ZD20	1410.4409.02
3.0 GHz, active, differential, 1 MΩ || 0.6 pF, R&S®ProbeMeter, micro button	R&S®RT-ZD30	1410.4609.02
4.5 GHz, active, differential, 1 MΩ || 0.4 pF, R&S®ProbeMeter, micro button	R&S®RT-ZD40	1410.5205.02
10 MHz, current, AC/DC, 0.01 V/A, 150 A (RMS)	R&S®RT-ZC10	1409.7750.02
100 MHz, current, AC/DC, 0.1 V/A, 30 A (RMS)	R&S®RT-ZC20	1409.7766.02
3.0 GHz, active, 1 MΩ || 0.8 pF, R&S®ProbeMeter, micro button	R&S®RT-ZS30	1410.4309.02
50 MHz, AC/DC, 0.1 V/A, 30 A (RMS), Rohde & Schwarz probe interface	R&S®RT-ZC15B	1409.8227.02
100 MHz, current, AC/DC, 0.1 V/A, 30 A (RMS), Rohde & Schwarz probe interface	R&S®RT-ZC20B	1409.8233.02
Probe accessories		
Accessory Set for R&S®RT-ZP10 passive probe (2.5 mm probe tip)	R&S®RT-ZA1	1409.7566.00
Spare Accessory Set for R&S®RT-ZS10/10E/20/30	R&S®RT-ZA2	1416.0405.02
Pin Set for R&S®RT-ZS10/10E/20/30	R&S®RT-ZA3	1416.0411.02
Mini Clips	R&S®RT-ZA4	1416.0428.02
Micro Clips	R&S®RT-ZA5	1416.0434.02
Lead Set	R&S®RT-ZA6	1416.0440.02
Pin Set for R&S®RT-ZD20/30	R&S®RT-ZA7	1417.0609.02
Pin Set for R&S®RT-ZD40	R&S®RT-ZA8	1417.0867.02
SMA Adapter	R&S®RT-ZA10	1416.0457.02
Probe Power Supply	R&S®RT-ZA13	1409.7789.02
Accessories		
Front Cover, for R&S®RTO digital oscilloscopes	R&S®RTO-Z1	1333.0096.02
Soft Case, for R&S®RTO digital oscilloscopes and accessories	R&S®RTO-Z3	1304.9118.02
Transit Case, for R&S®RTO/RTE digital oscilloscopes and accessories	R&S®RTO-Z4	1317.7025.02
Probe Pouch, for R&S®RTO digital oscilloscopes	R&S®RTO-Z5	1317.7031.02
USB 2.0 Compliance Test Fixture Set	R&S®RT-ZF1	1317.3420.02
Ethernet Compliance Test Fixture Set	R&S®RT-ZF2	1317.5522.02
Probe Deskew and Calibration Test Fixture	R&S®RT-ZF20	1800.0004.02
Probe Set for E and H Near-Field Measurements, 9 kHz to 1 GHz	R&S®HZ-14	1026.7744.03
Compact Probe Set for E and H Near-Field Measurements, 30 MHz to 3 GHz	R&S®HZ-15	1147.2736.02
3 GHz, 20 dB Preamplifier, 100 V to 230 V Power Adapter, for R&S®HZ-15	R&S®HZ-16	1147.2720.02
19" Rackmount Kit, for R&S®RTO digital oscilloscopes with 6 HU	R&S®ZZA-RTO	1304.8286.02

Service options		
Extended Warranty, one year	R&S®WE1	Please contact your local Rohde & Schwarz sales office.
Extended Warranty, two years	R&S®WE2	
Extended Warranty with Calibration Coverage, one year	R&S®CW1	
Extended Warranty with Calibration Coverage, two years	R&S®CW2	

Extended warranty with a term of one and two years (WE1 and WE2)
Repairs carried out during the contract term are free of charge [13]. Necessary calibration and adjustments carried out during repairs are also covered.

Extended warranty with calibration (CW1 and CW2)
Enhance your extended warranty by adding calibration coverage at a package price. This package ensures that your Rohde & Schwarz product is regularly calibrated, inspected and maintained during the term of the contract. It includes all repairs [13] and calibration at the recommended intervals as well as any calibration carried out during repairs or option upgrades.

[13] Excluding defects caused by incorrect operation or handling and force majeure. Wear-and-tear parts are not included.

Service that adds value

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- Uncompromising quality
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The Rohde&Schwarz electronics group offers innovative solutions in the following business fields: test and measurement, broadcast and media, secure communications, cybersecurity, radiomonitoring and radiolocation. Founded more than 80 years ago, this independent company has an extensive sales and service network and is present in more than 70 countries. The electronics group is among the world market leaders in its established business fields. The company is headquartered in Munich, Germany. It also has regional headquarters in Singapore and Columbia, Maryland, USA, to manage its operations in these regions.

Sustainable product design

- Environmental compatibility and eco-footprint
- Energy efficiency and low emissions
- Longevity and optimized total cost of ownership

Certified Quality Management
ISO 9001

Certified Environmental Management
ISO 14001

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