## **SPECIFICATIONS**

## Number of Output Channels: 2

D.C. Output Characteristics

Output Voltage Range:  $\pm 5$  V into 50  $\Omega$ ;  $\pm 10$  V into >10 k $\Omega$ 

load.

Maximum output current: ±100 mA

Output impedance:  $50 \pm .5 \Omega$ 

Minimum amplitude range: <100  $\mu V$  full-scale into 50  $\Omega$ 

D.C. Output Accuracy: (at calibrate time): 0.5% FSR into

50.00 Ω for FS≥500 mV

1.0% FSR  $\pm$  500  $\mu$ V into 50.0  $\Omega$  for FS <500 mV. (Accuracy

gradually drops from .5% to 1% at 50 mV FS)

0.3% FSR into user supplied load of from 49  $\Omega$  to 1 M $\Omega$  for

FSR  $\geq 10\%$  of Max Output Voltage Range.

Output Temperature Coefficient: <0.01% of FSR/ °C typical

Waveform DAC Resolution: 12 bits

Gain Adjust Resolution: 0.05% Amplitude

Offset Adjust Resolution: 0.05% FSR

Waveform DAC Int. Non-Linearity: ±0.03% typ.; ±0.05%

max

Waveform DAC Diff. Non-Linearity: ±0.75 lsb typ; ±1 lsb

max, monotonic

Offset Adjust Range: ± Full Scale Amplitude (wrt midscale of

waveform); must be within Output Voltage range.

**Dynamic Characteristics:** 

Risetime/Falltime:  $\leq 8$  nsec (5.5 nsec typ) Overshoot and Ringing:  $\leq 5\%$ , typically 2%

Total Harmonic Distortion:  $\leq -65$  dBc, f < 200 kHz (1 V rms into 50  $\Omega$ )  $\leq -55$  dBc, f < 1 MHz

 $\leq$  -45 dBc, f < 5 MHz

Spurious and non-harmonic distortion:

< -65 dBc, f  $\leq 1$  MHz < -60 dBc, f > 1 MHz excluding the band within

1 kHz of carrier

Settling Time:

< 20 nsec to 1% typical,

50 nsec max.

Interchannel Crosstalk:  $\leq 0.05\%$ , tested with both channels at

10 V amplitude.

Channel-to-Channel Analog Delay Difference: ≤ 3 nsec