



HP 16550A¹

Timing analysis rate	Conventional: 250/500 MHz* Transitional: 125/250 MHz* Glitch: 125 MHz
State analysis rate	100 MHz
Channels/card	102/51*
Clock channels	6 (of the 102 above)
Memory depth/channel	4 K/8 K*
Setup/hold time	3.5/0 ns to 0/3.5 ns adj. in 500 ps steps
Minimum detectable glitch	3.5 ns
Probe input R and C	100 k Ω and \sim 8 pF
Trigger terms	Patterns: 10 Ranges: 2 Edge and glitch: 2 Timers: 2
Trigger sequence levels	12 in state and 10 in timing
Labels	126
Symbols	1000