

Table 1-1. HP 16520/21A Specifications.

HP 16520A/21A SPECIFICATIONS**CLOCK SOURCES** (HP 16520A ONLY)**INTERNAL CLOCK**

Clock Period: Programmable from 20 ns to 200 μ s in a 1-2-5 sequence.

Data Period Accuracy: $\pm 2\%$ (of period) ± 1 ns

EXTERNAL CLOCK (provided by user)

Input Clock Period:

1 Hz to 50 MHz (20 ns min period) ECL or TTL, internal frequency divide (/1, /5, or /10) provided.

Duty cycle: 10 ns minimum high time, 10 ns minimum low time

STROBES (HP 16520A ONLY)

Number of Strokes: 3 (ECL or TTL)

Bits/Channel: 4095

Maximum Bit Rate: 20 MBit/s (50 ns Period)

Edge Placement:

≤ 10 MBits/s: tenths of period
 > 10 MBits/s to 20 MBits/s: fifths of Period
 (DELAY + WIDTH \leq PERIOD)

Minimum delay is 0/10 (0/5), maximum delay is 9/10 (4/5) data period.
 Minimum width is 1/10 (1/5) of data period, maximum width is the data period (values in parentheses apply to 20 MBits/s timebase setting).

If strobes are desired while operating with external clock, the data rate will be divided to 1/5 or 1/10 the external clock rate.